

LED: GREEN IS POSITIVE

00000000
0001234567

000010010101015
006010112345

TP GND1

J1

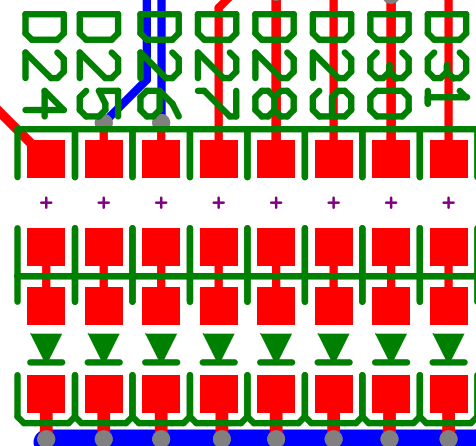
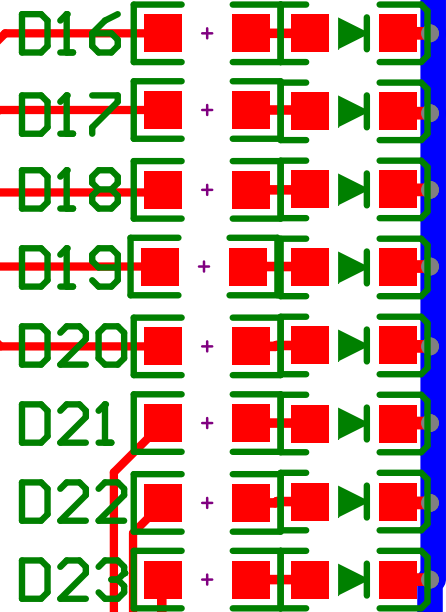
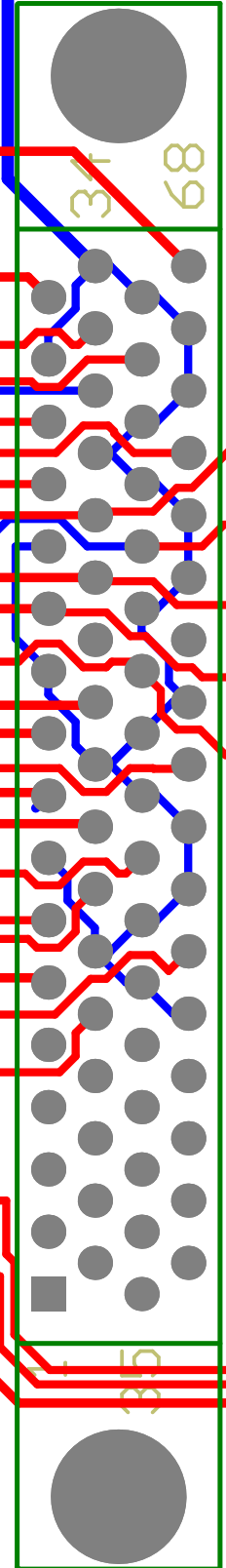
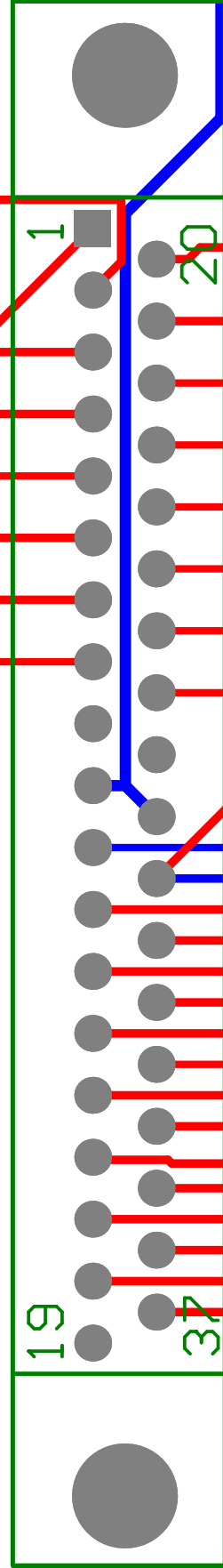
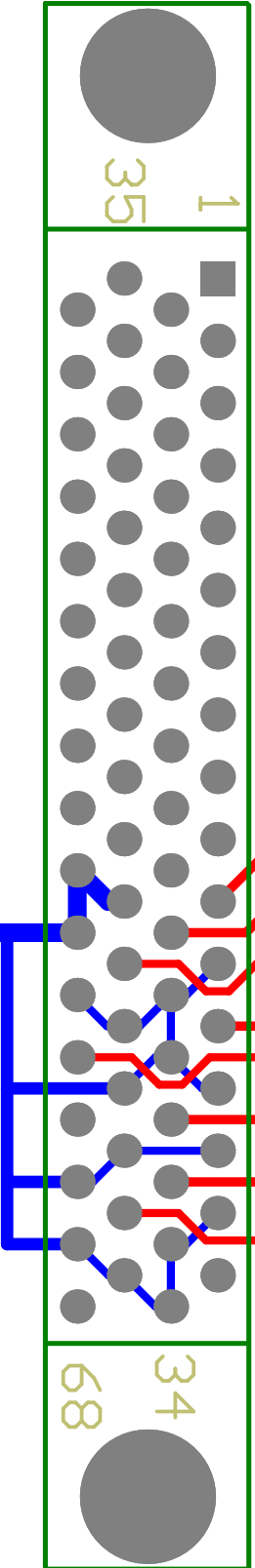
J3

J2

35 1

1 20

34 68



PXI-6723

PXI-6723

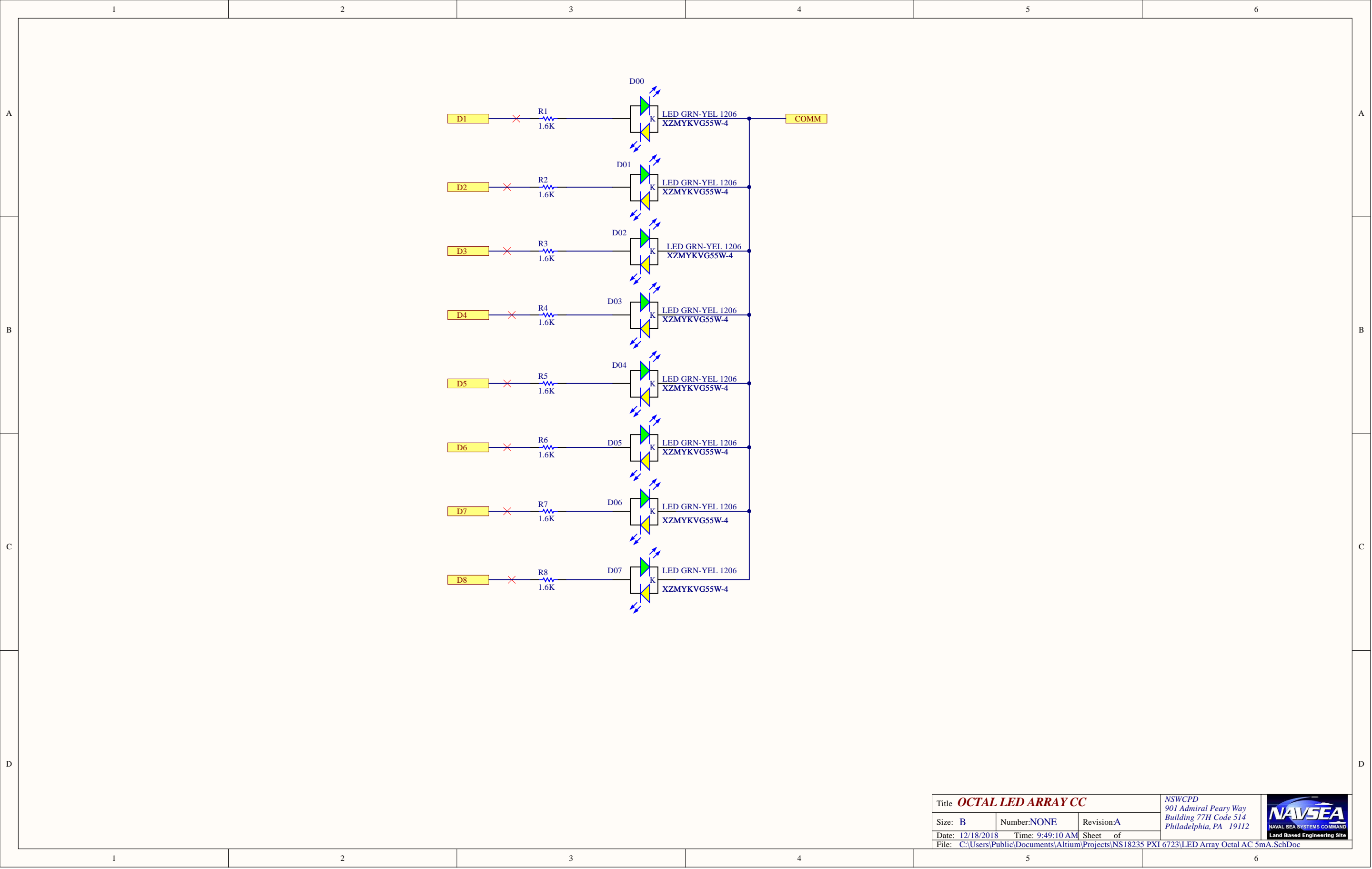
NSWCPD LBES NS18235 REV. A

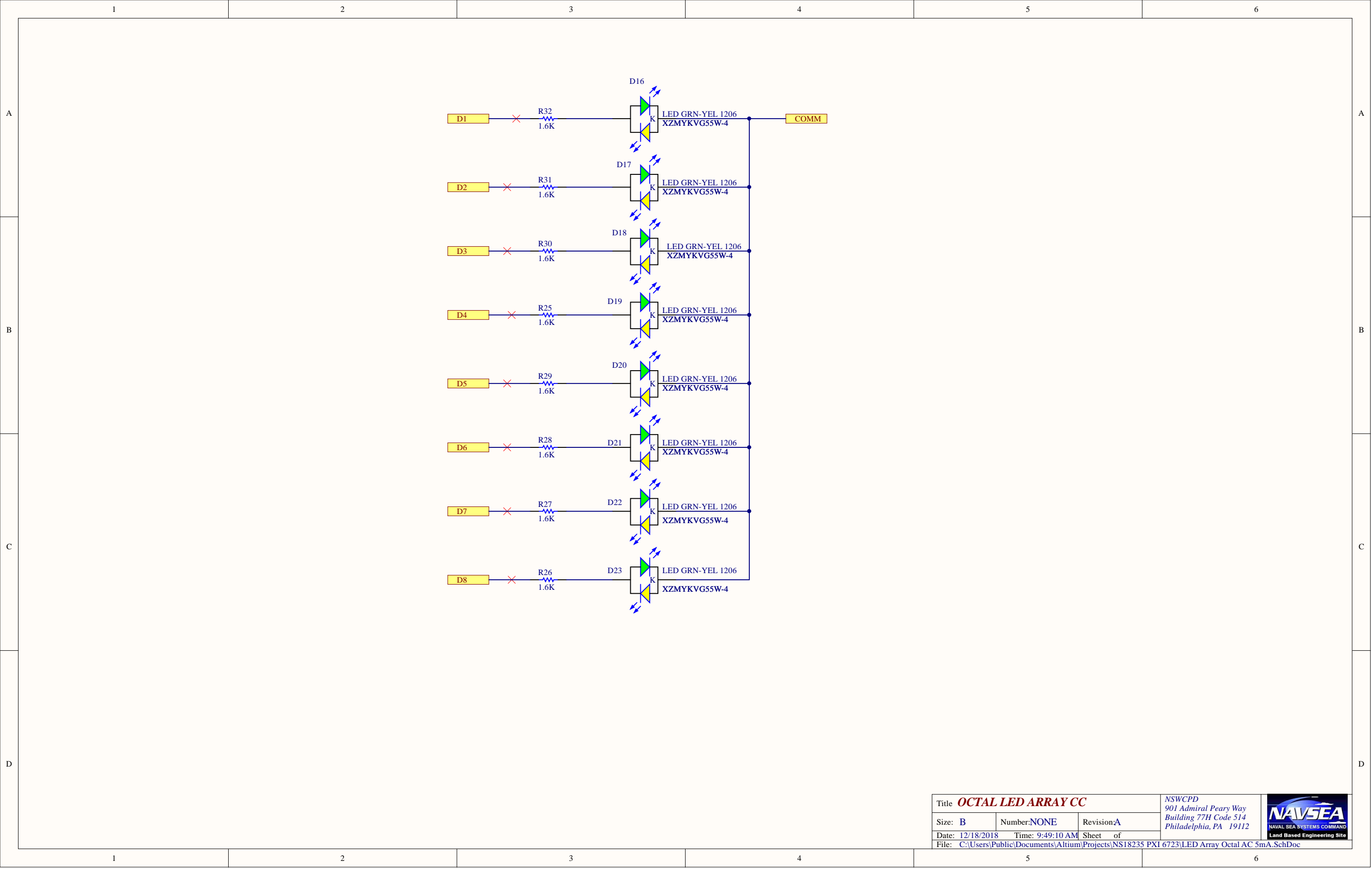
NI 9205

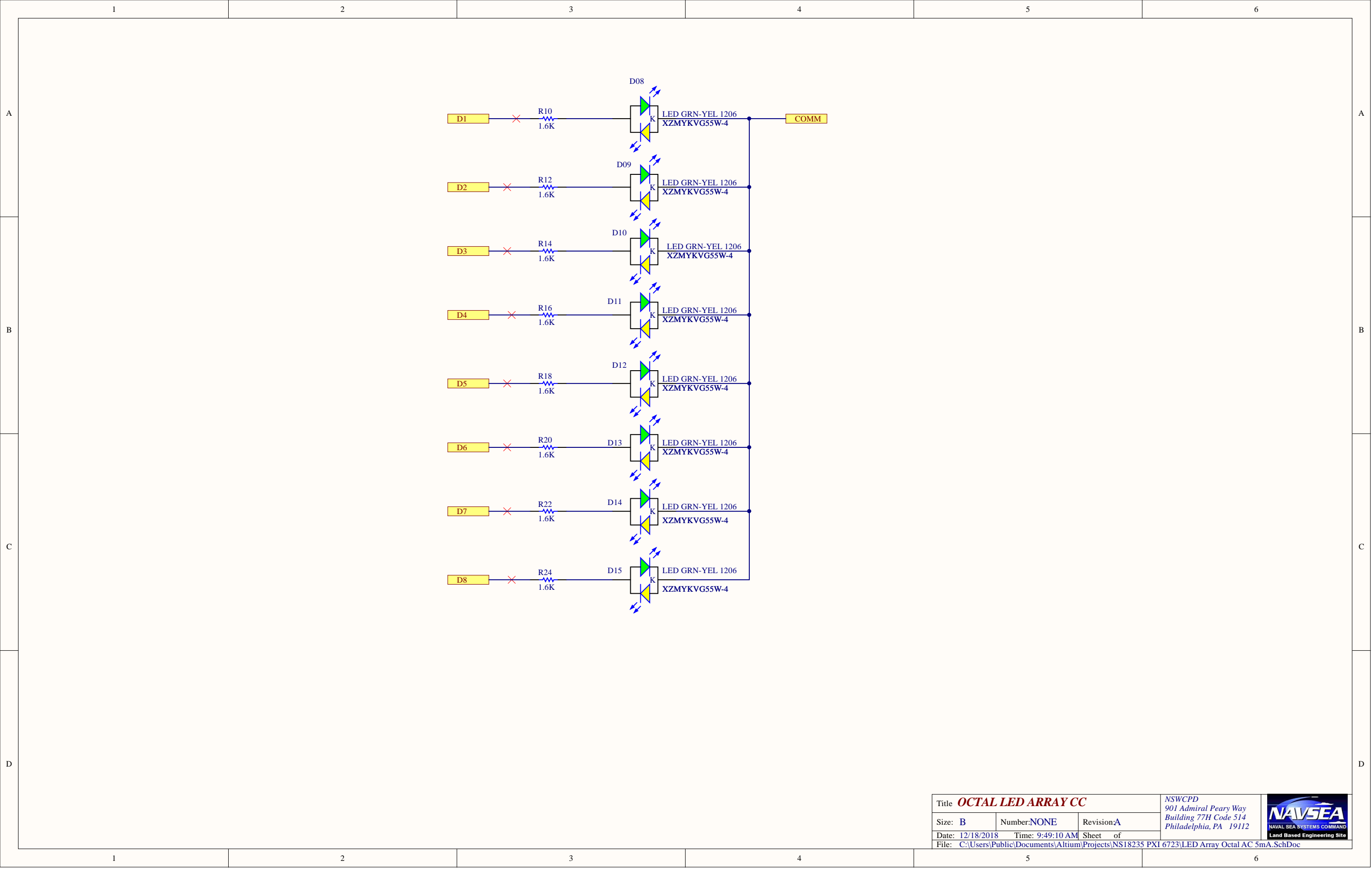
PXI 6723 TO NI 9205 SIGNAL TESTER

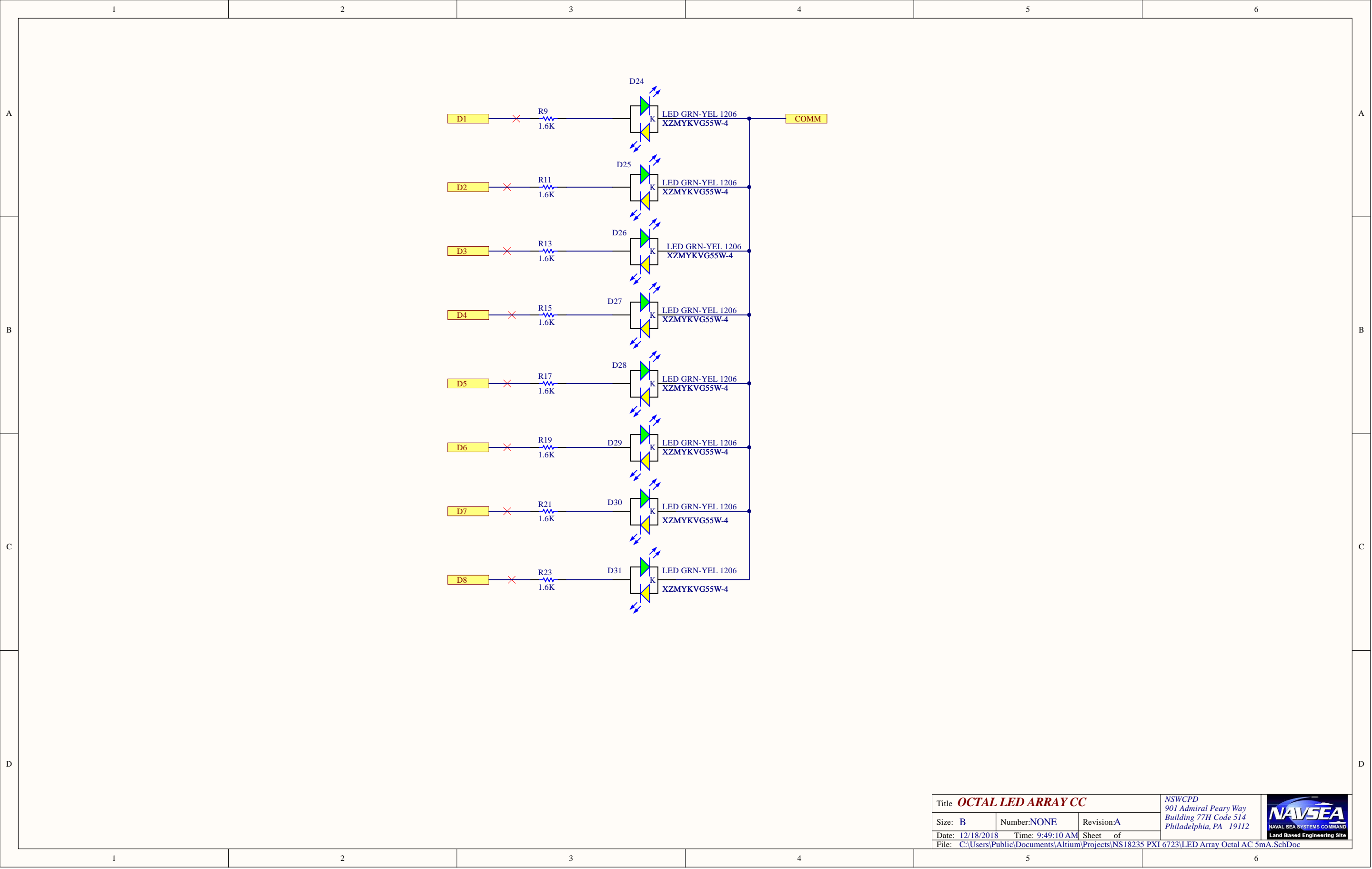
3650.00

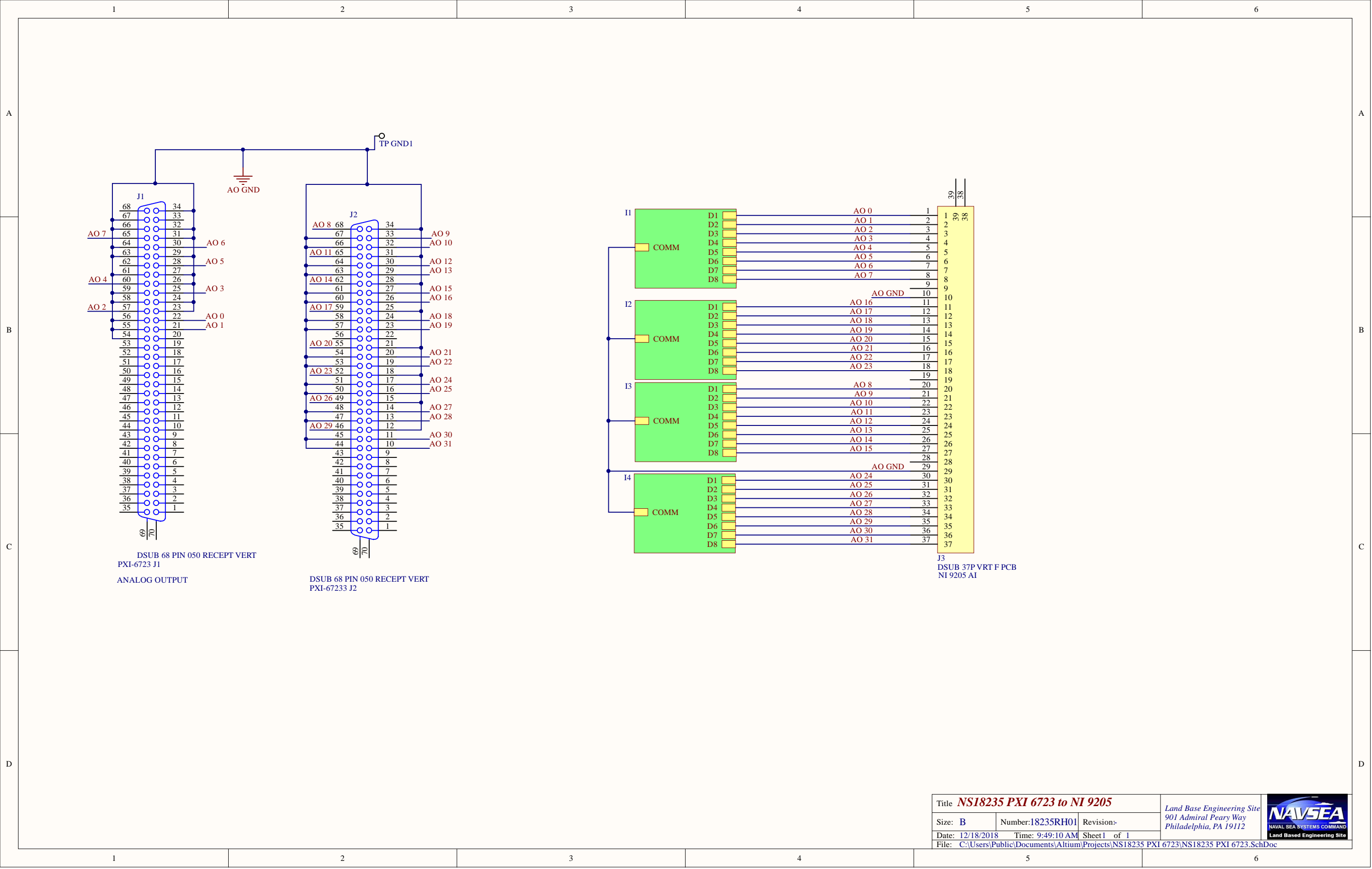
4400.00











LED: GREEN IS POSITIVE

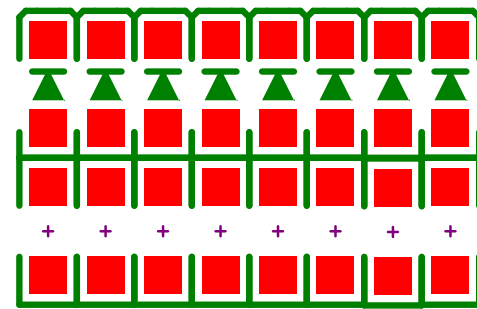
00000000
00100000
00200000
00300000
00400000
00500000
00600000
00700000

J3

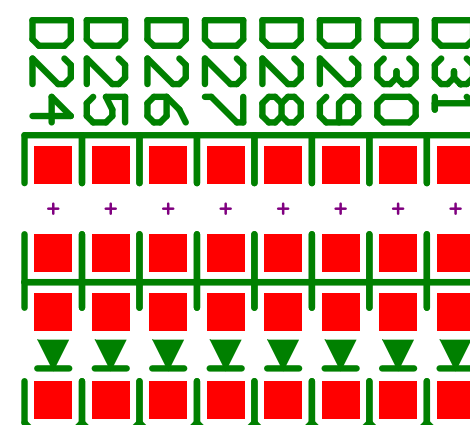
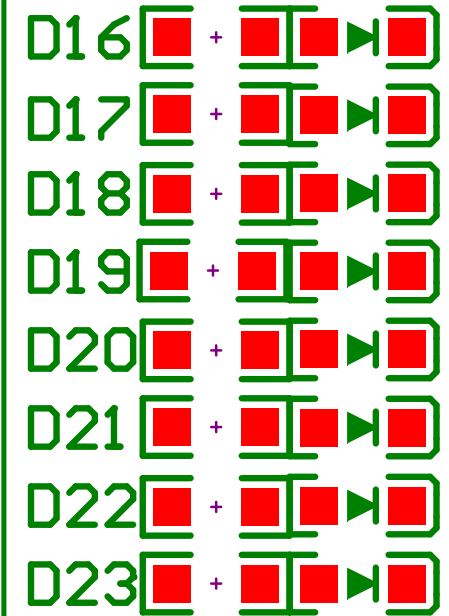
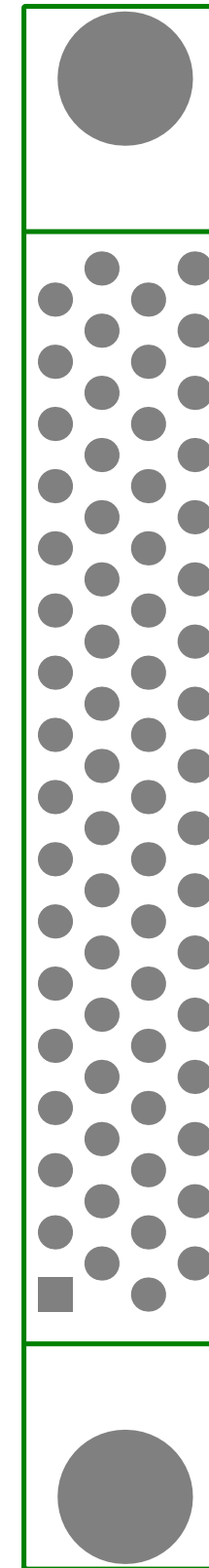
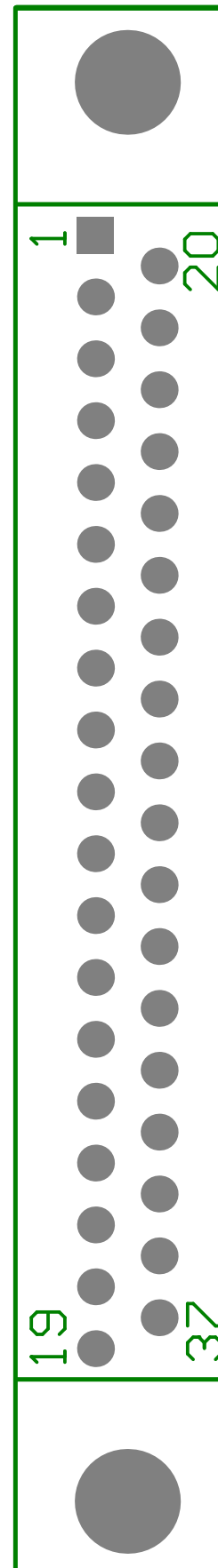
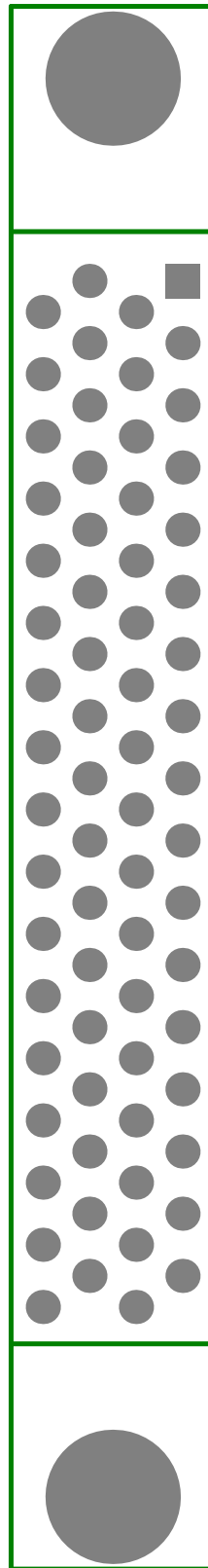
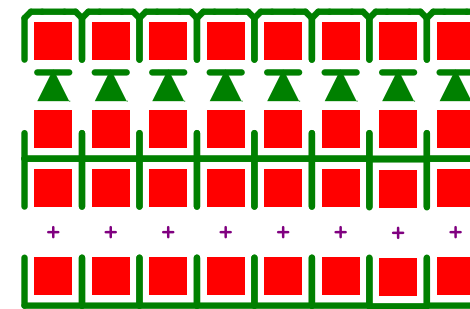
00000000
00100000
00200000
00300000
00400000
00500000
00600000
00700000

TP GND1

J1



J2



PXI-6723

PXI-6723

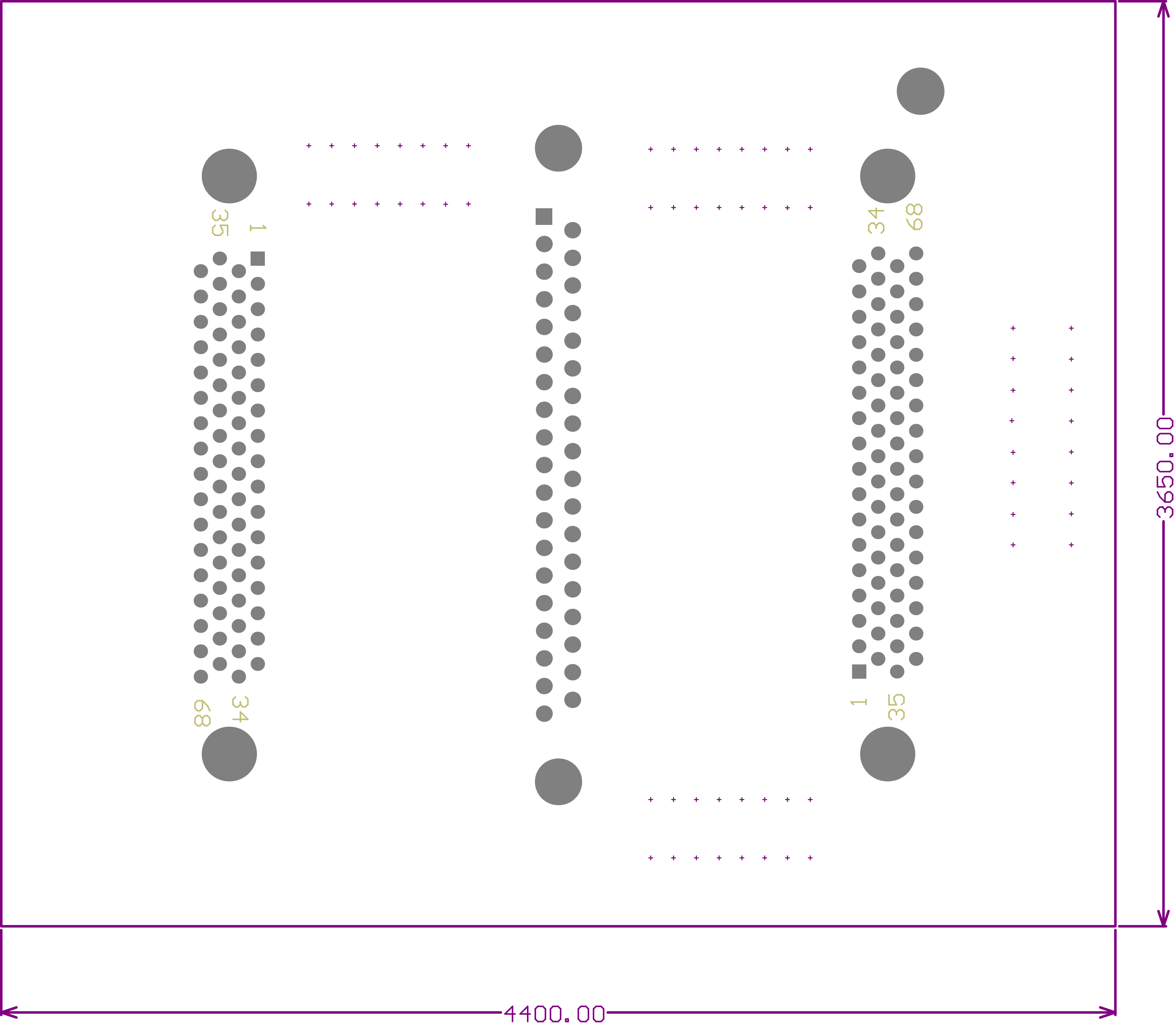
NSWCPD LBES NS18235 REV. A

NI 9205

















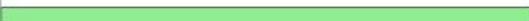
PXI 6723 TO NI 9205 SIGNAL TESTER

3650.00

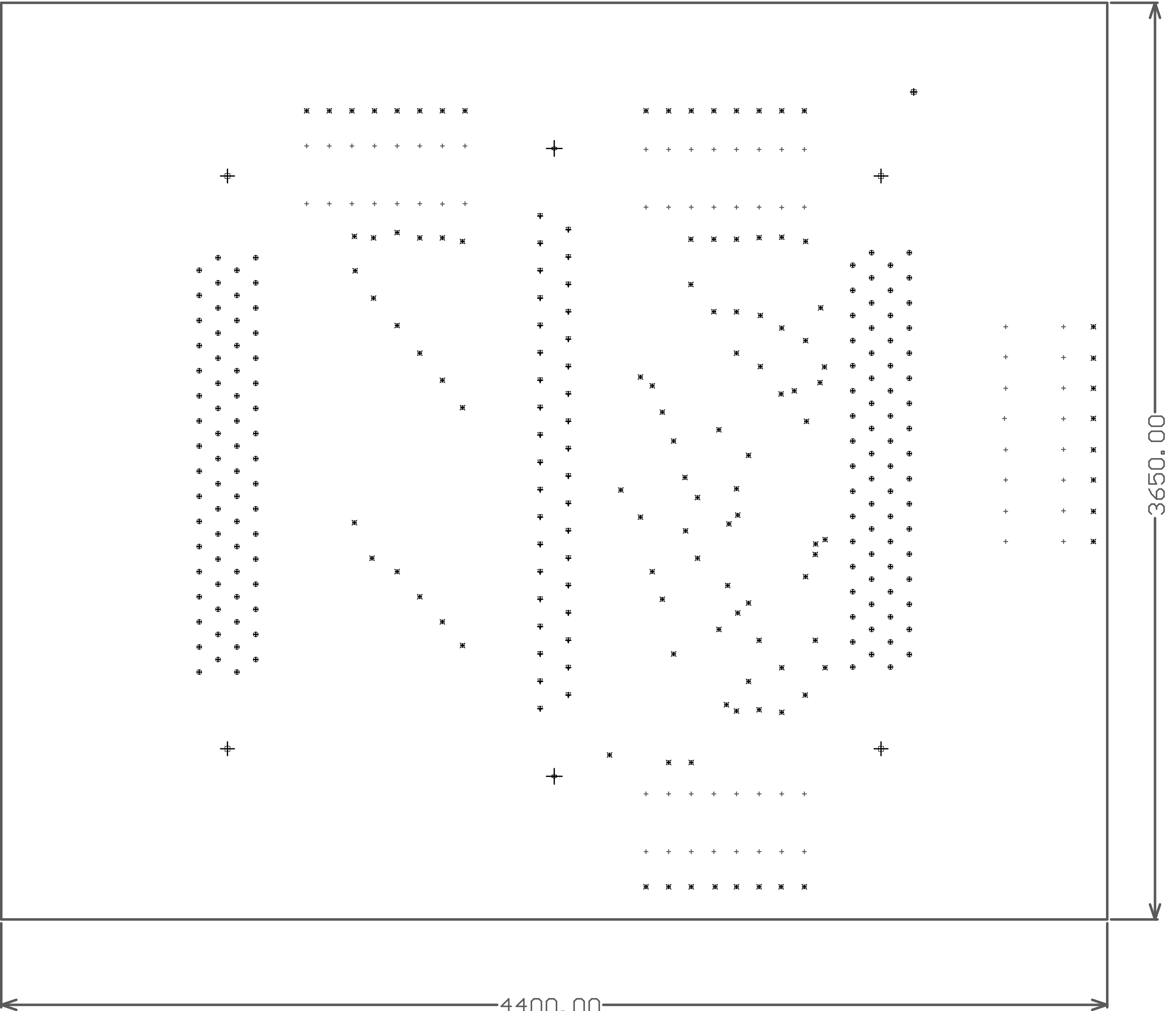
4400.00



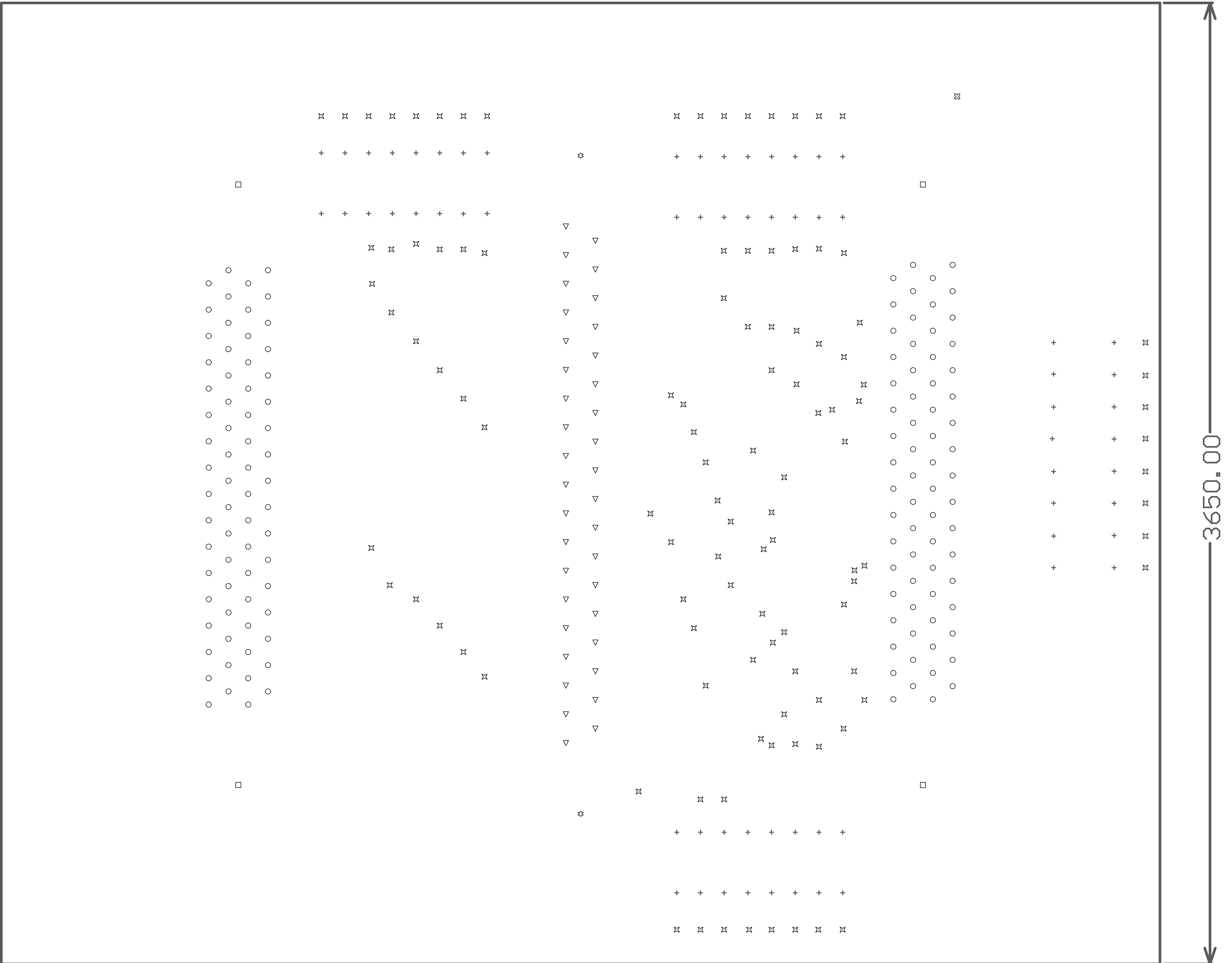
Board Stack Report

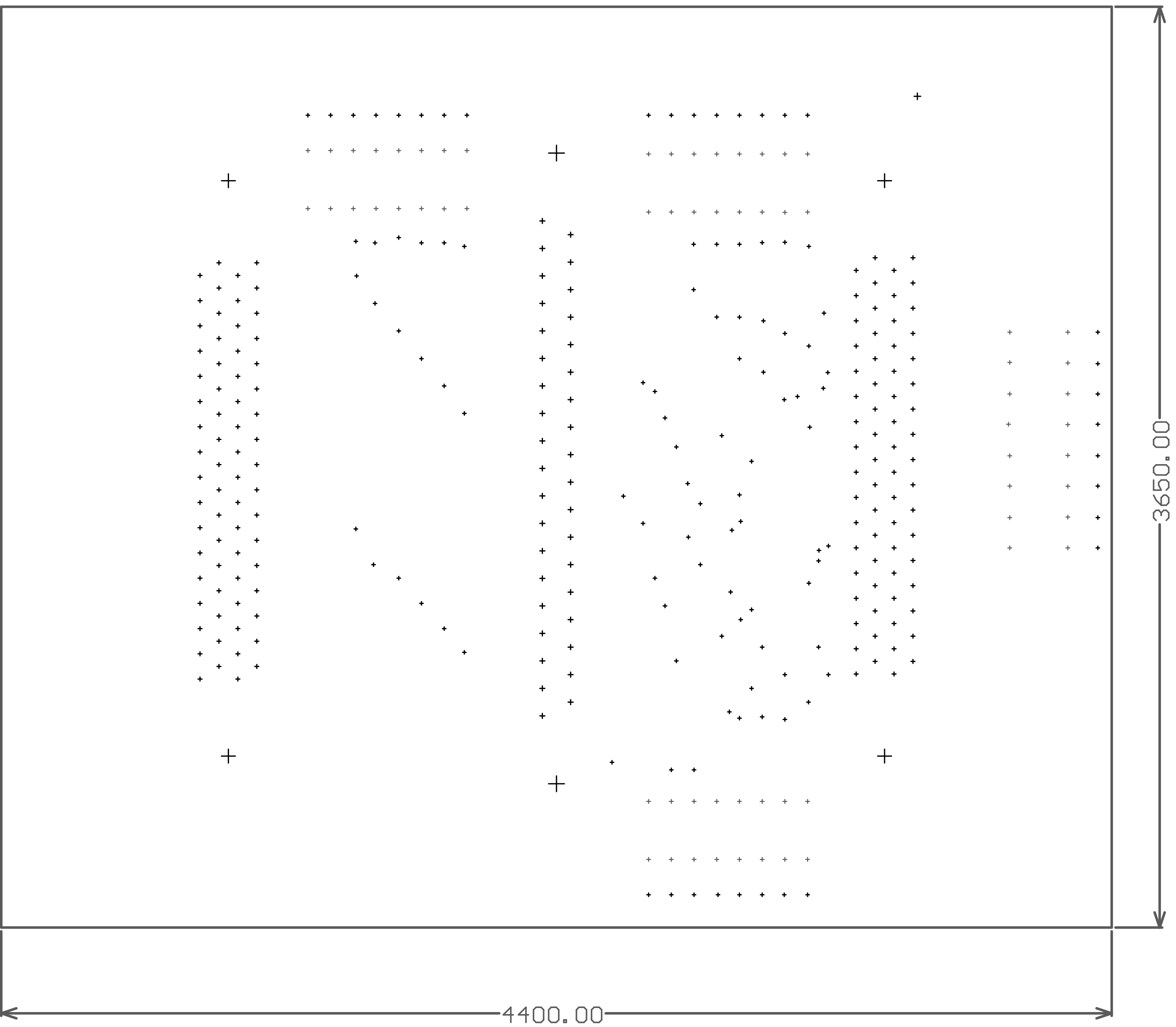
Stack Up		Layer Stack			
Layer	Board Layer Stack	Name	Material	Thickness	Constant
1		Top Paste			
2		Top Overlay			
3	  	Top Solder	Solder Resist	0.40mil	3.5
4	  	Top Layer	Copper	1.70mil	
5		Dielectric 1	FR-4	59.00mil	4.2
6	  	Bottom Layer	Copper	1.70mil	
7	  	Bottom Solder	Solder Resist	0.40mil	3.5
8		Bottom Overlay			
9		Bottom Paste			
	Height : 63.20mil				

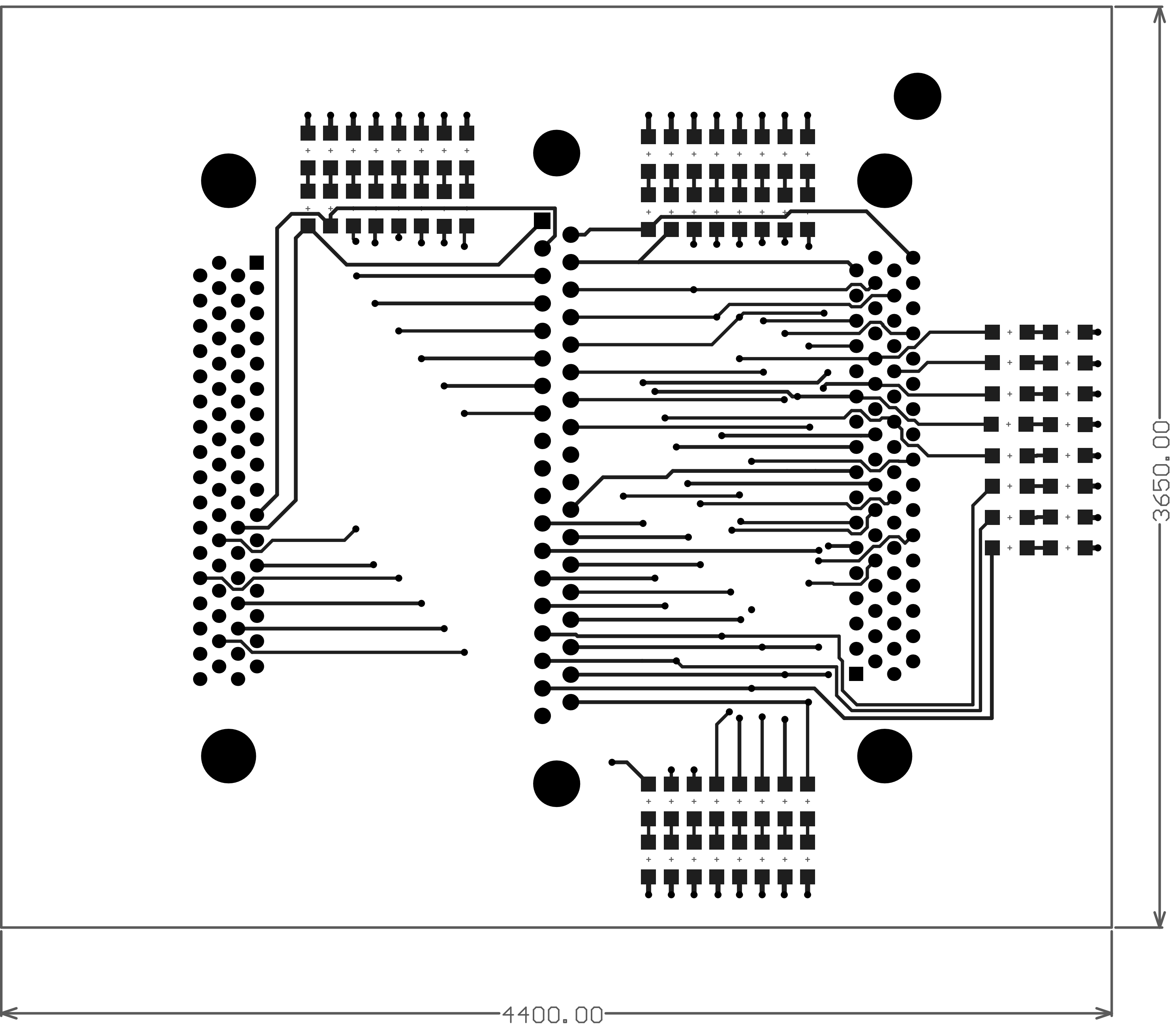
Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Via/Pad	Pad Shape	Template	Description	Hole Tolerance (+)	Hole Tolerance (-)
⊠	1	52.00mil (1.321mm)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c475h132			
☆	2	125.00mil (3.175mm)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c470h318			
□	4	110.24mil (2.800mm)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c550h280			
▽	37	40.00mil (1.016mm)	PTH	Round	Top Layer - Bottom Layer	Pad	(Mixed)	(Mixed)			
✕	109	14.00mil (0.356mm)	PTH	Round	Top Layer - Bottom Layer	Via	Rounded	v71h36			
○	136	31.50mil (0.800mm)	PTH	Round	Top Layer - Bottom Layer	Pad	(Mixed)	(Mixed)			
	289 Total										

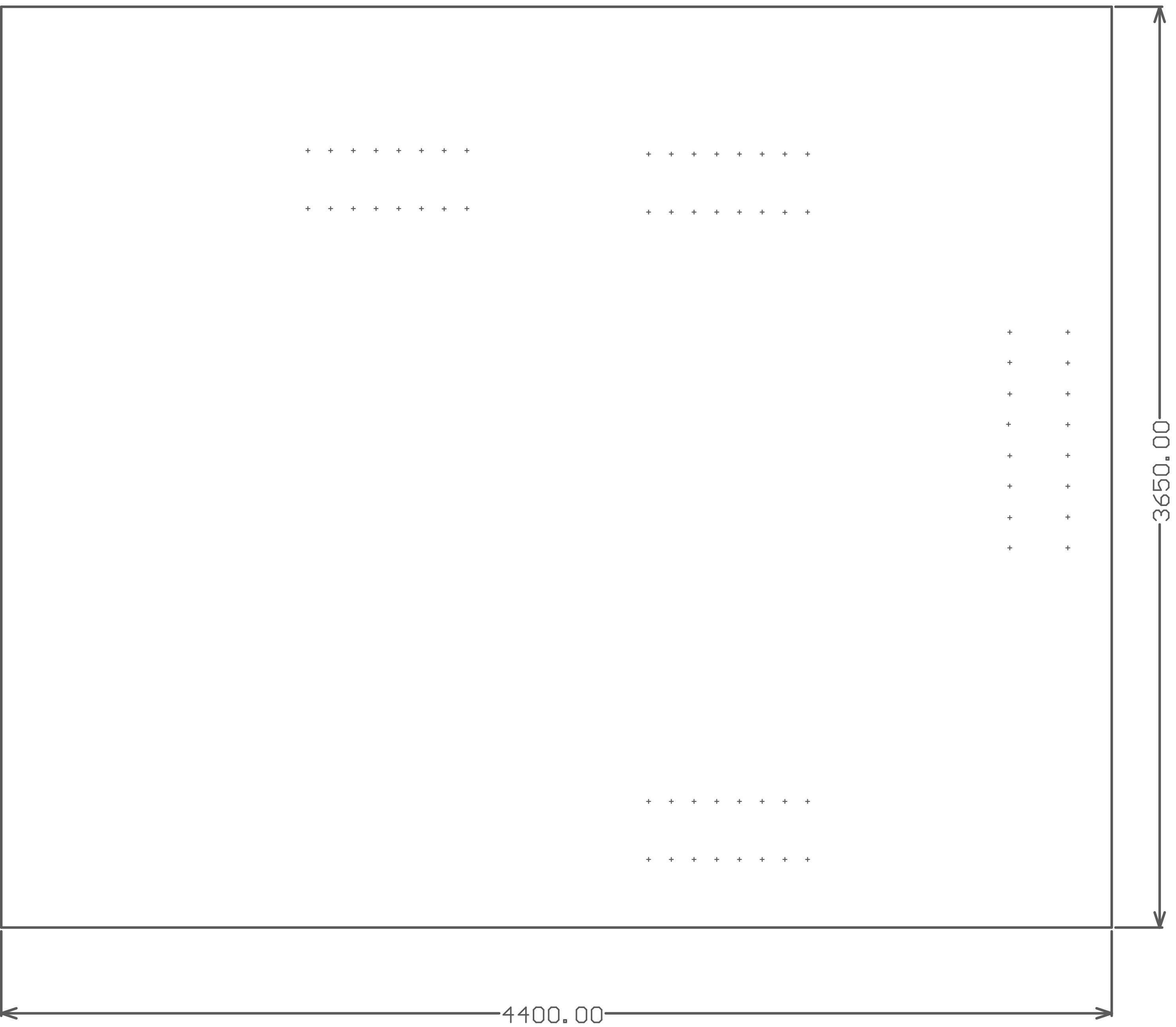


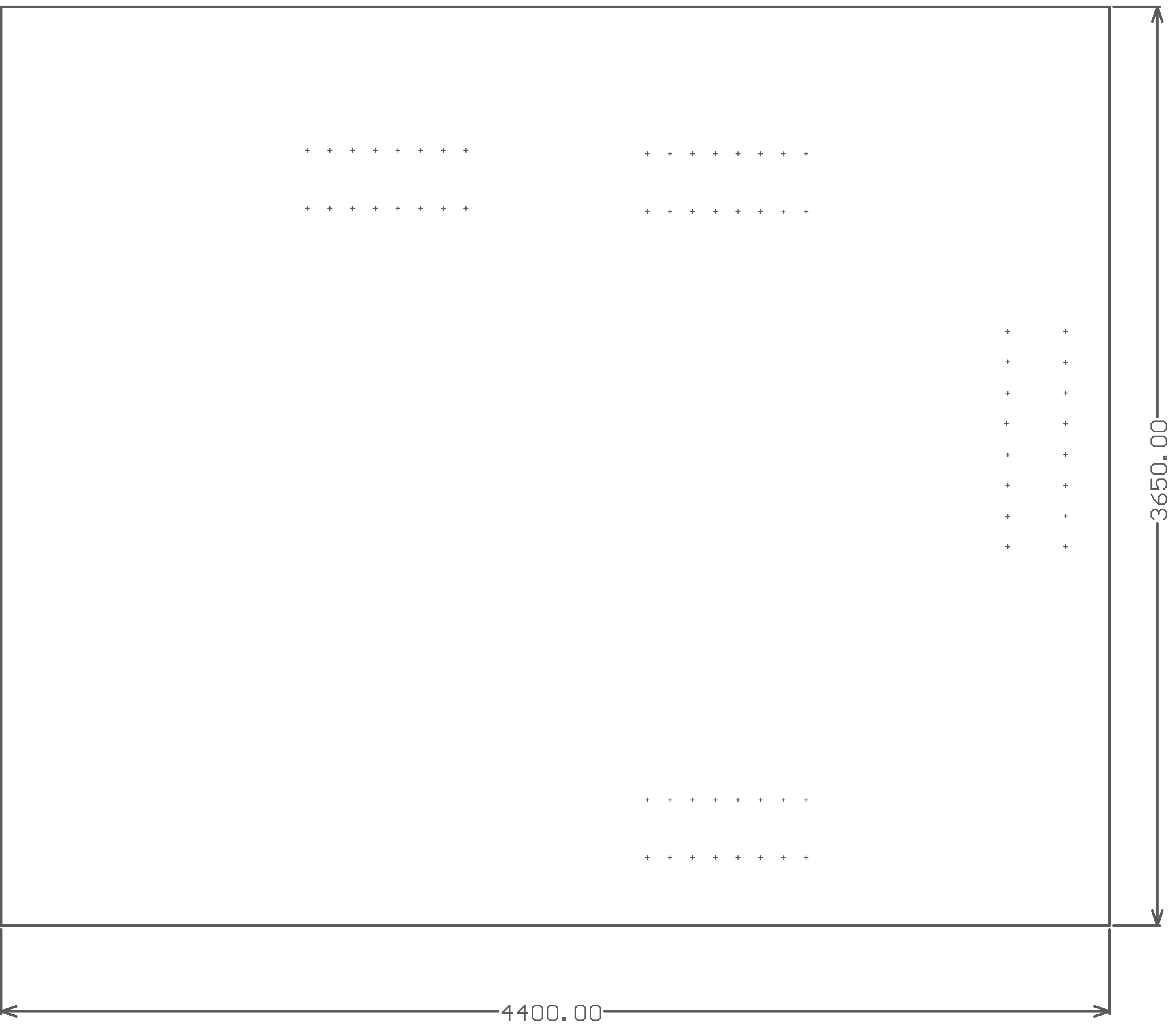
Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Via/Pad	Pad Shape	Template	Description	Hole Tolerance (+)	Hole Tolerance (-)
⊠	1	52.00mil (1.321mm)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c475h132			
☆	2	125.00mil (3.175mm)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c470h318			
□	4	110.24mil (2.800mm)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c550h280			
▽	37	40.00mil (1.016mm)	PTH	Round	Top Layer - Bottom Layer	Pad	(Mixed)	(Mixed)			
✕	109	14.00mil (0.356mm)	PTH	Round	Top Layer - Bottom Layer	Via	Rounded	v71h36			
○	136	31.50mil (0.800mm)	PTH	Round	Top Layer - Bottom Layer	Pad	(Mixed)	(Mixed)			
	289 Total										

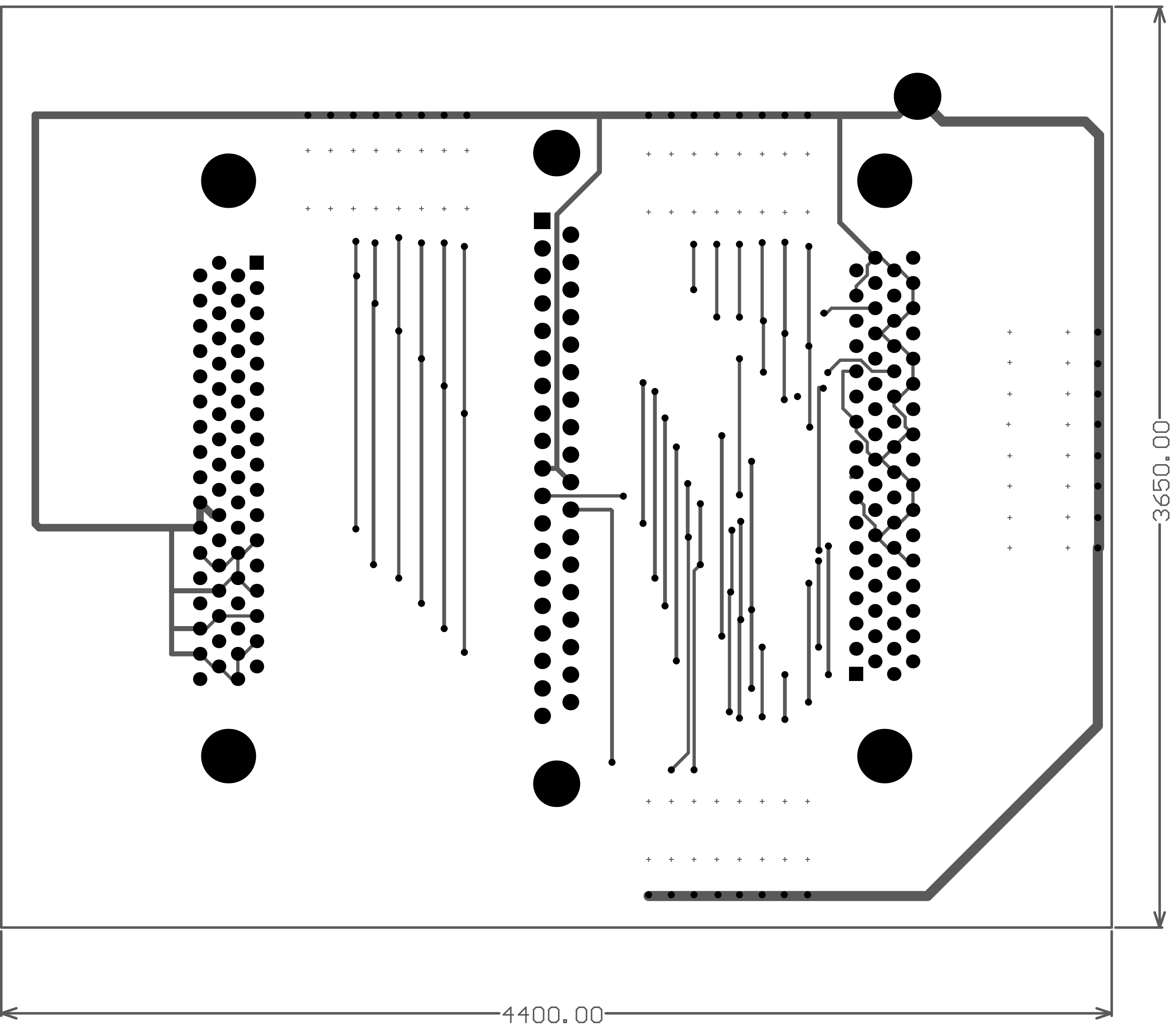


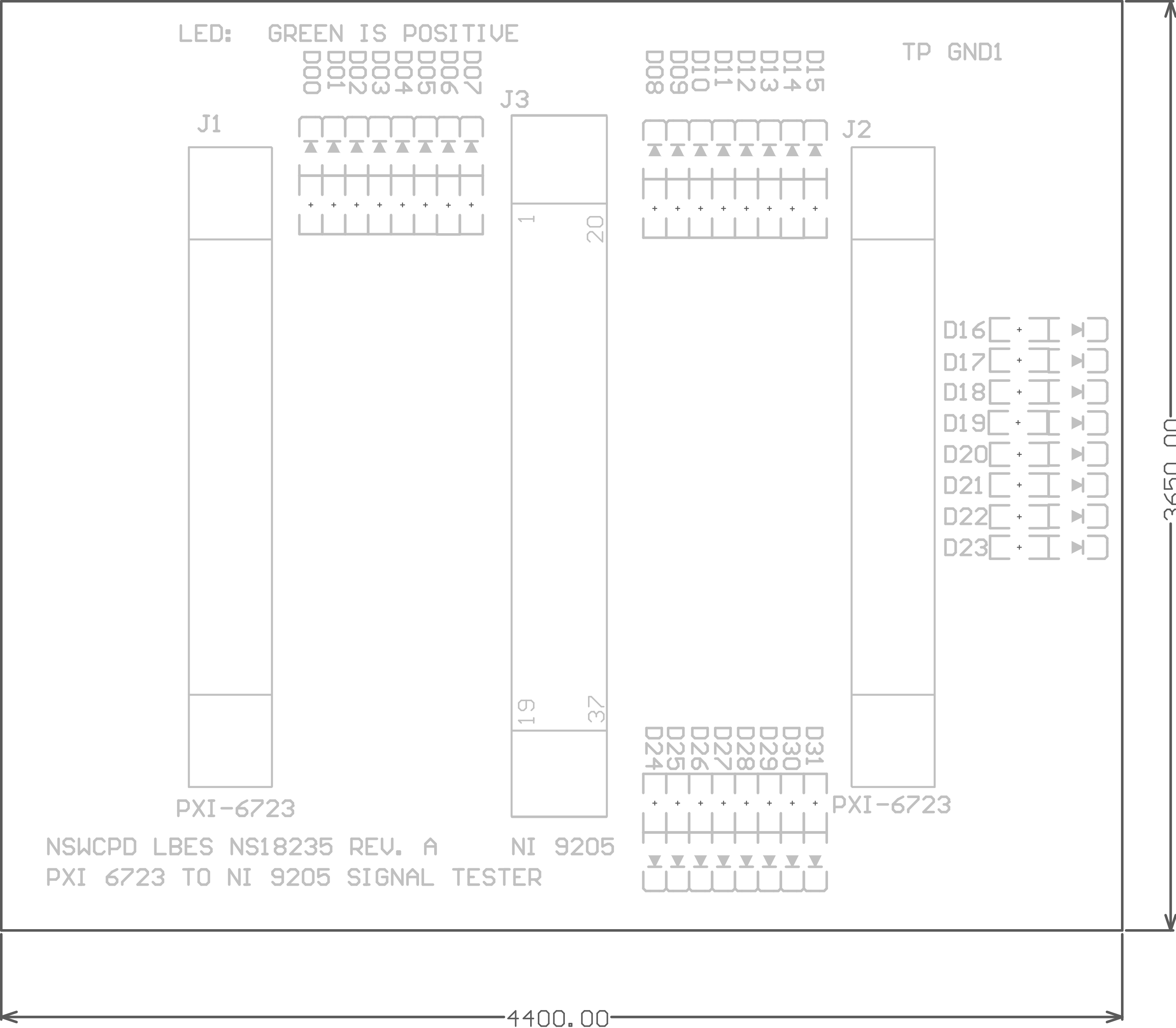


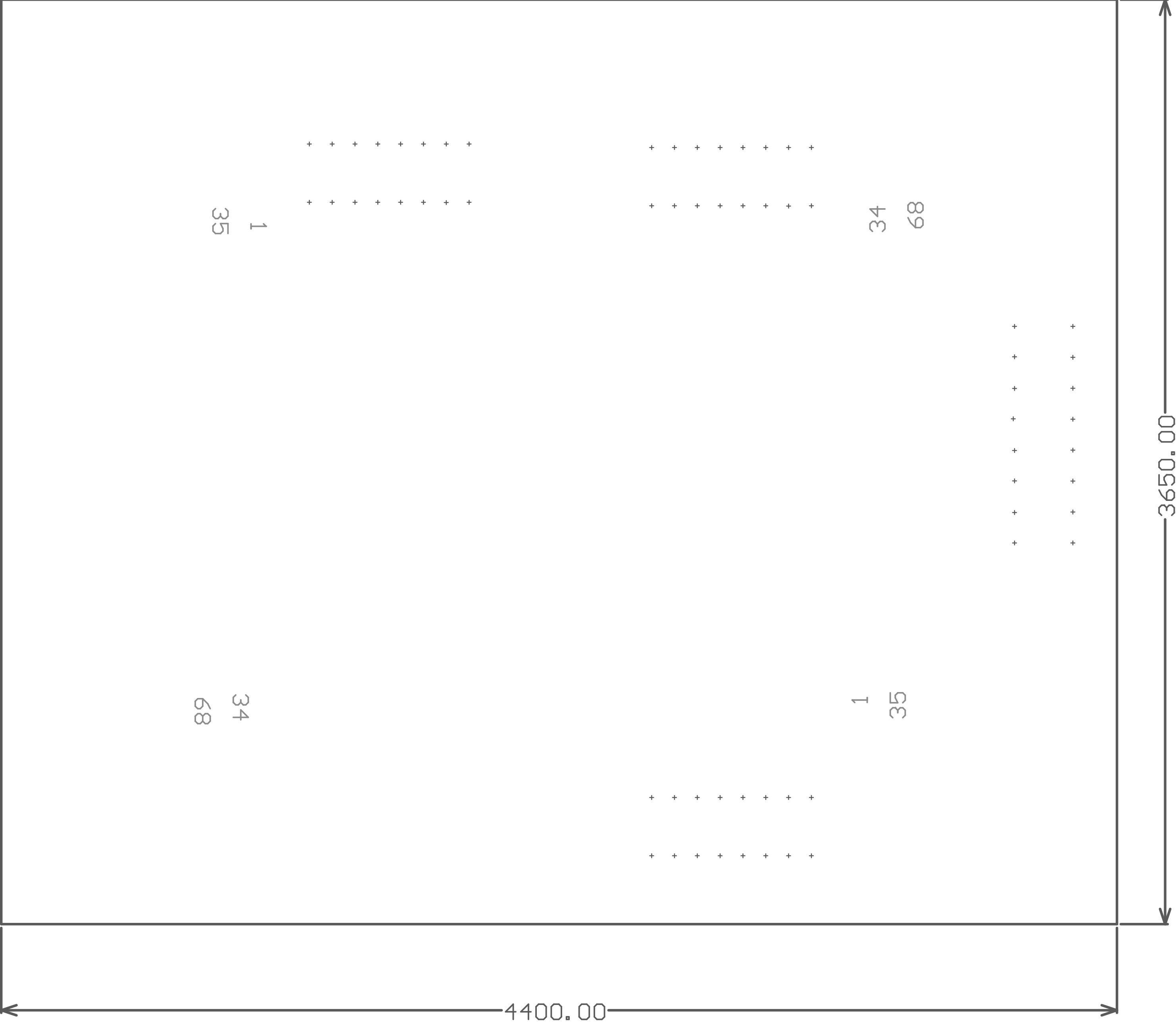


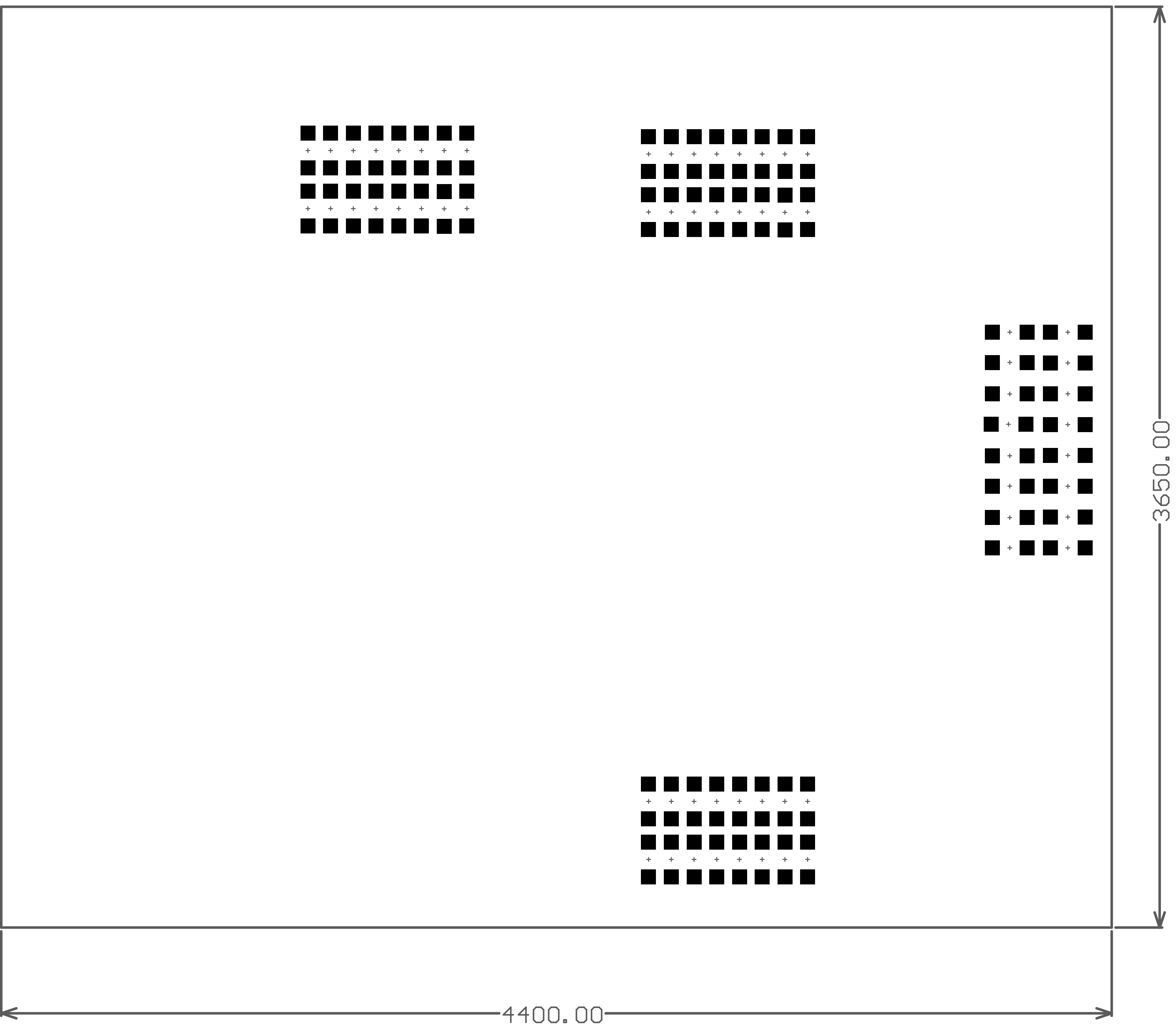


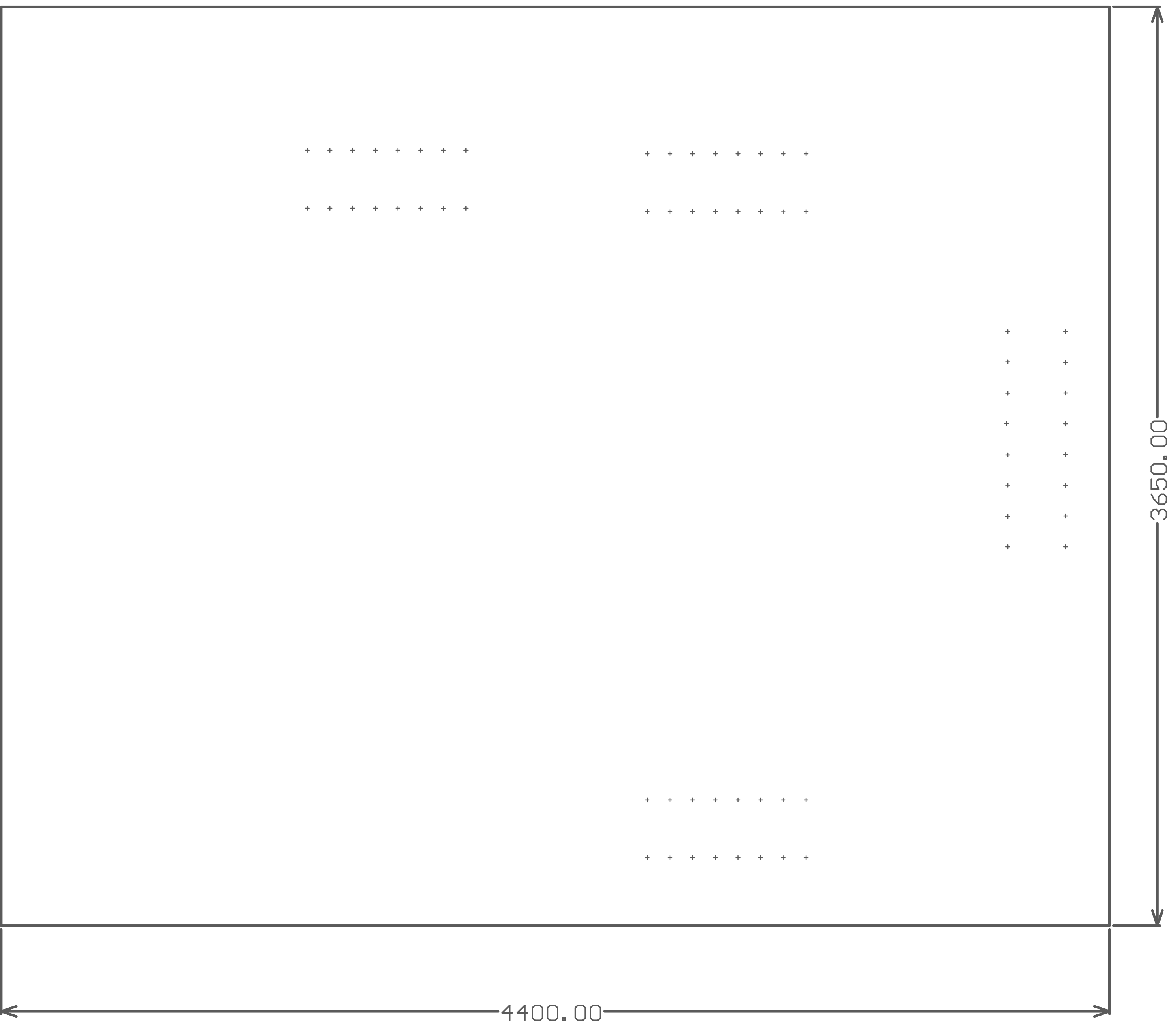


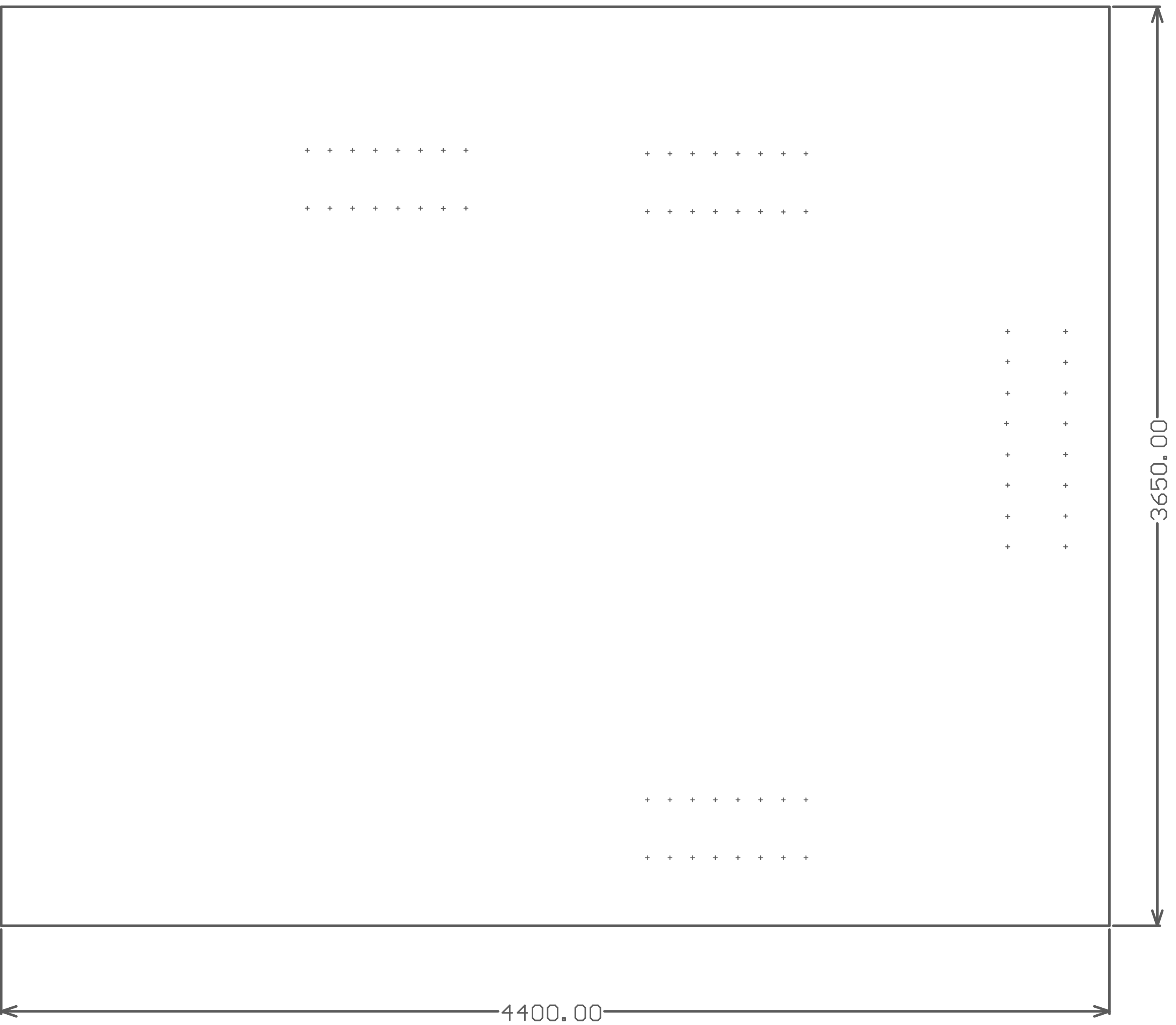


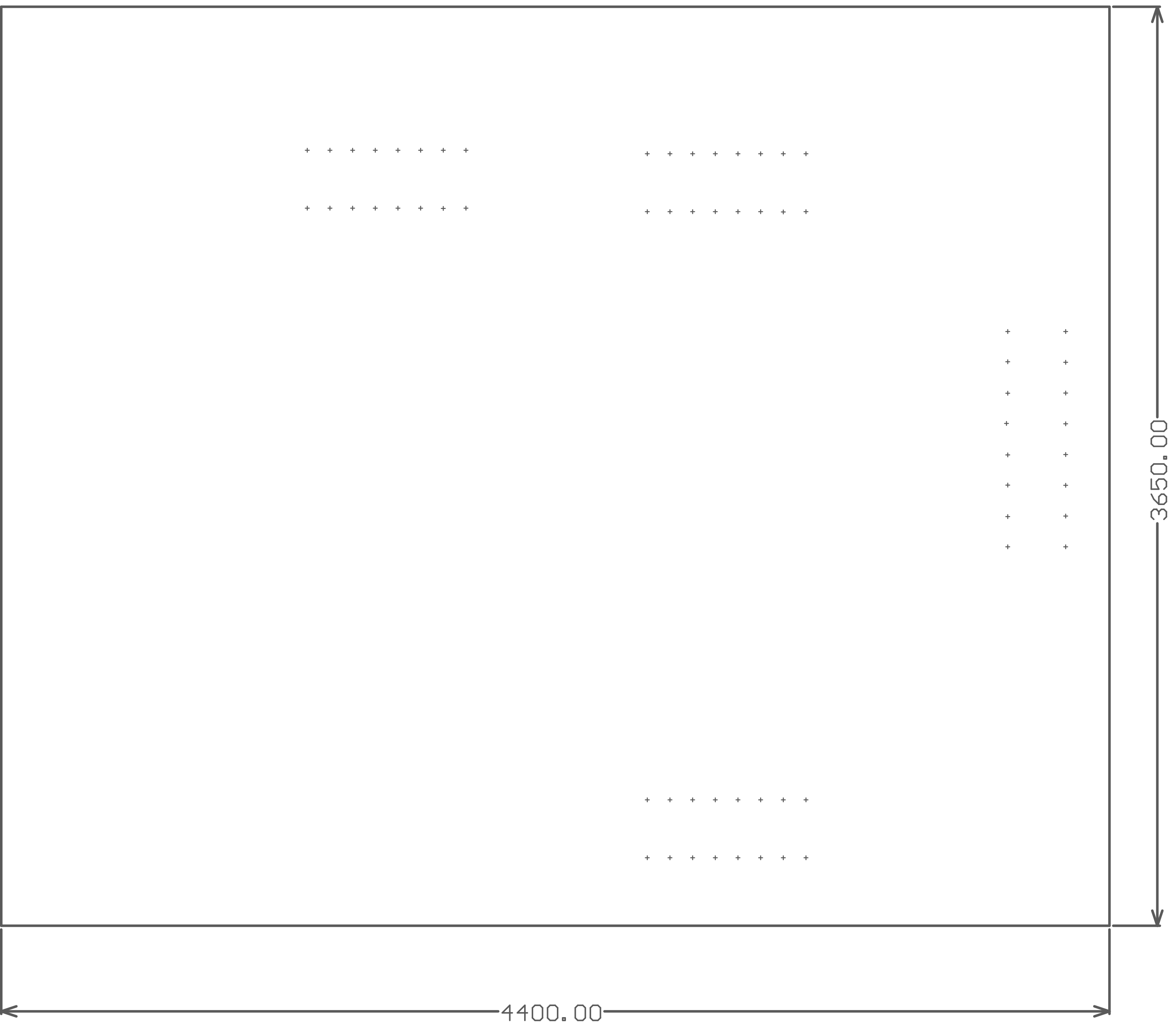


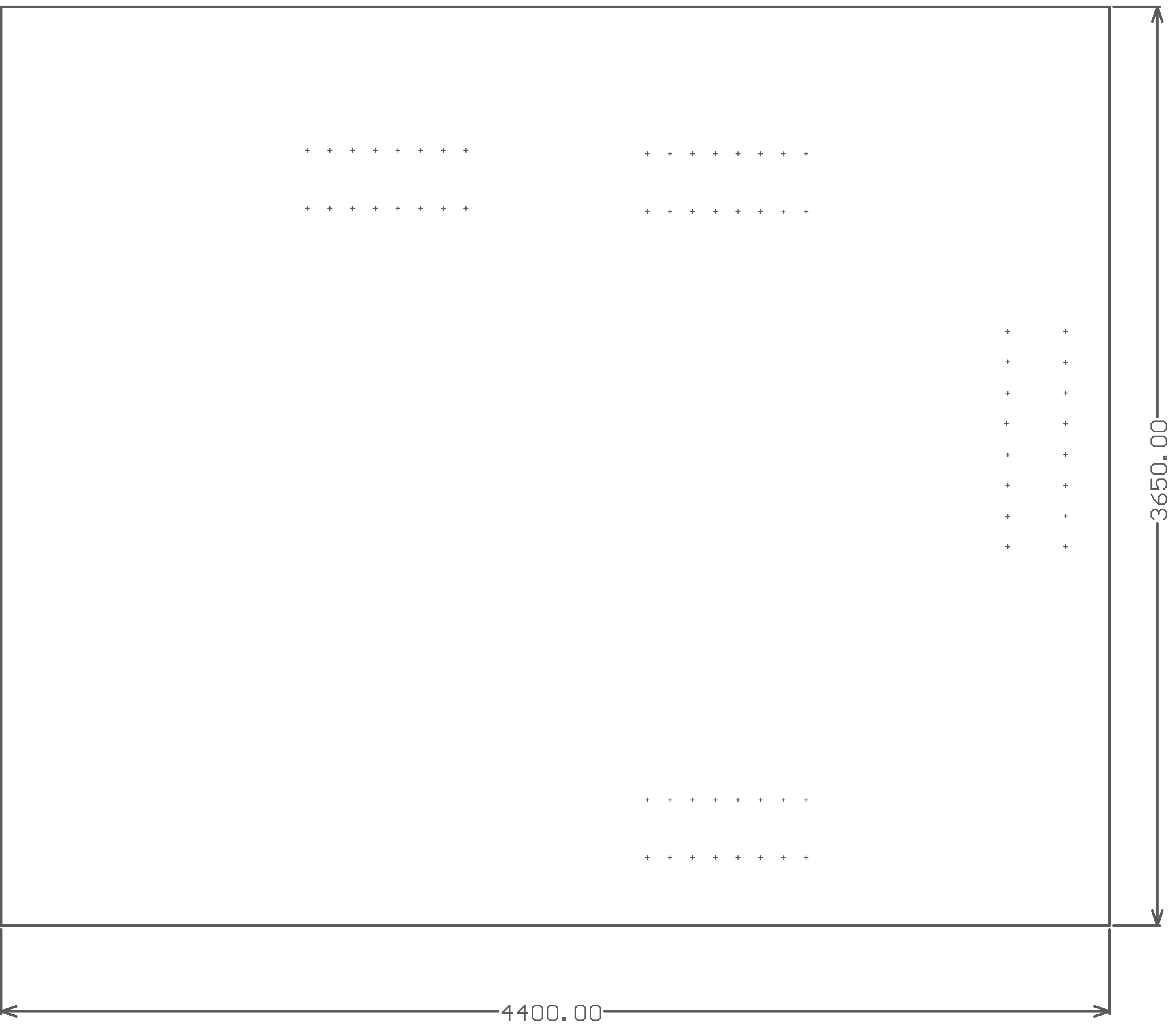


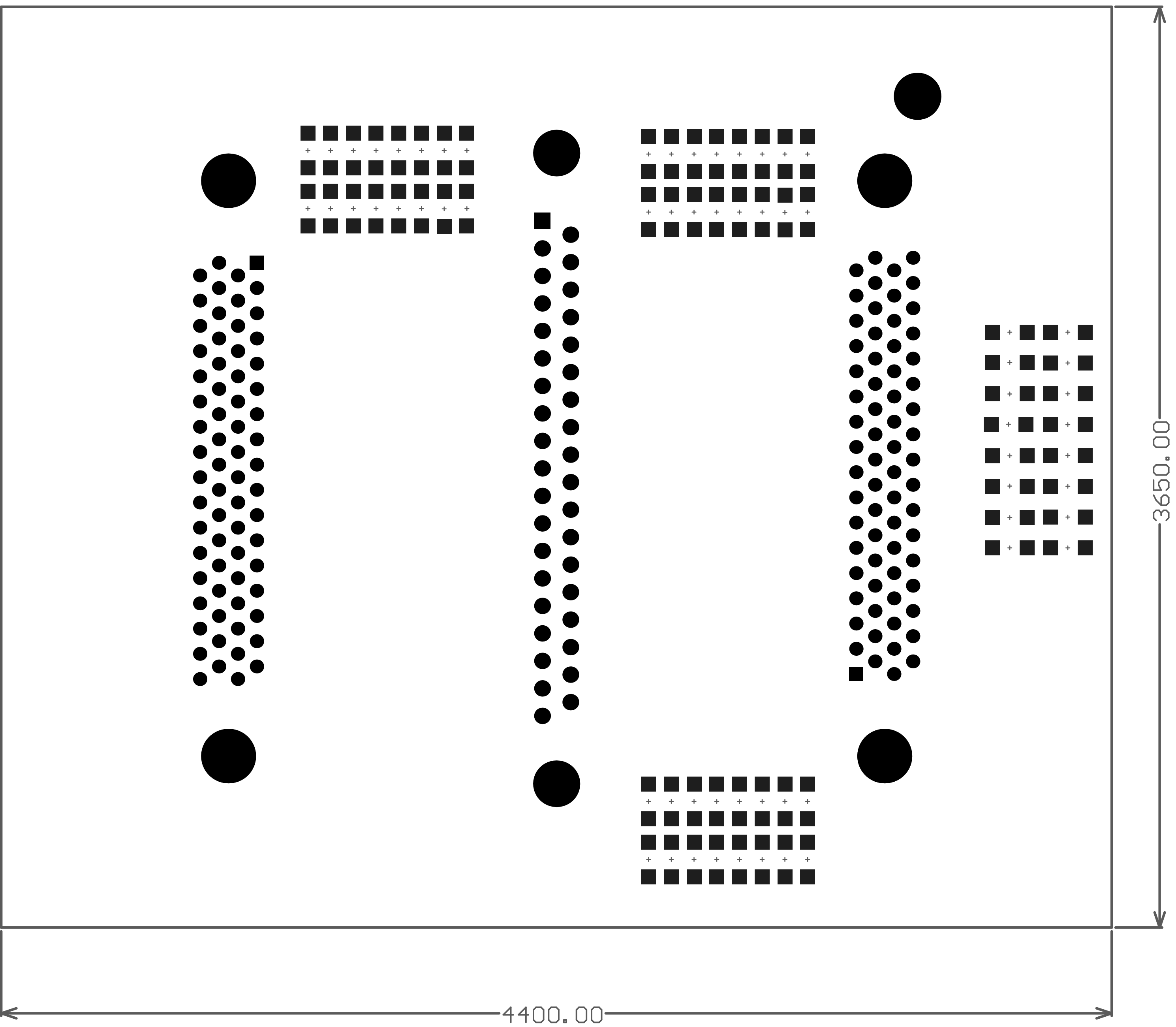


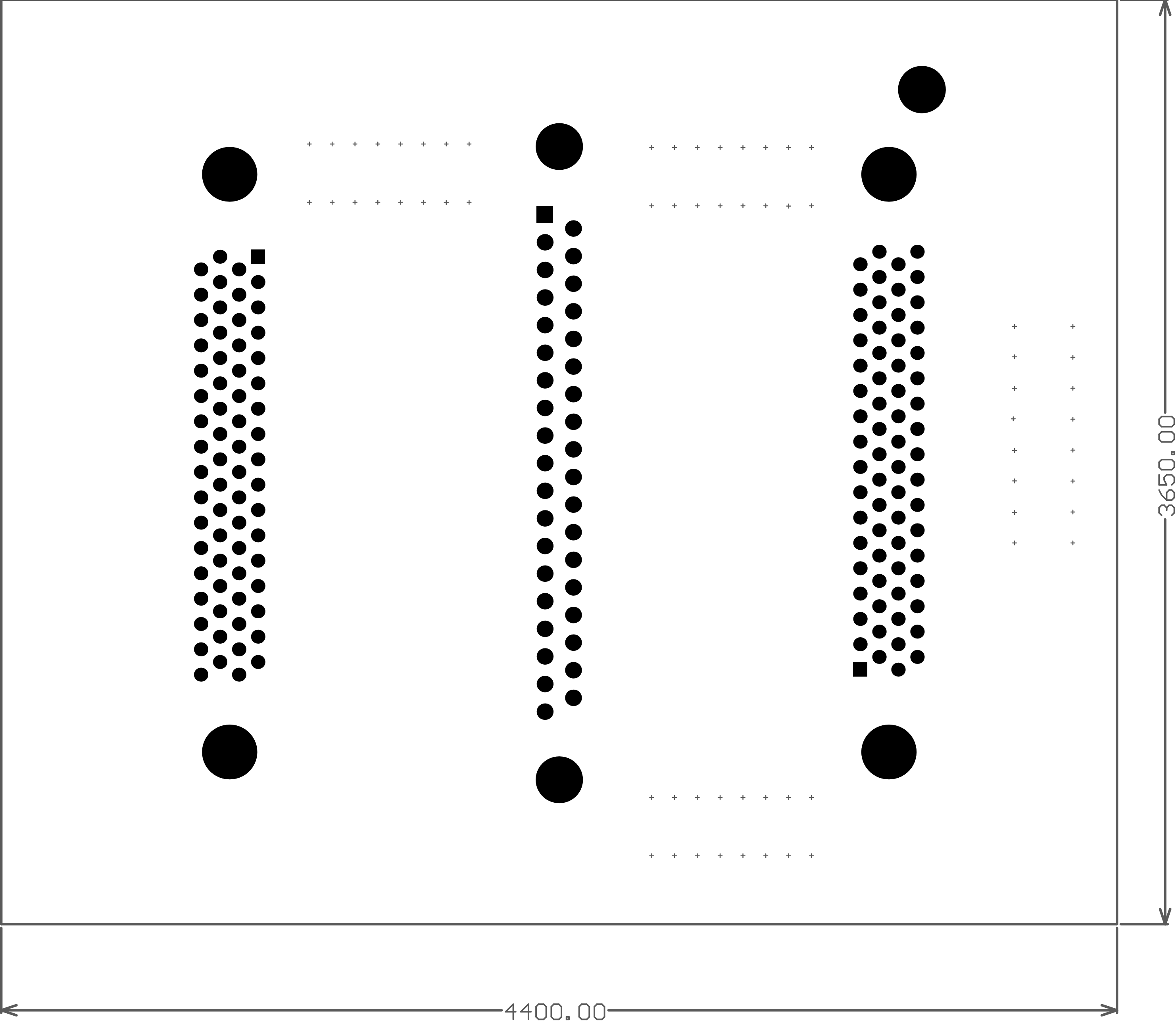




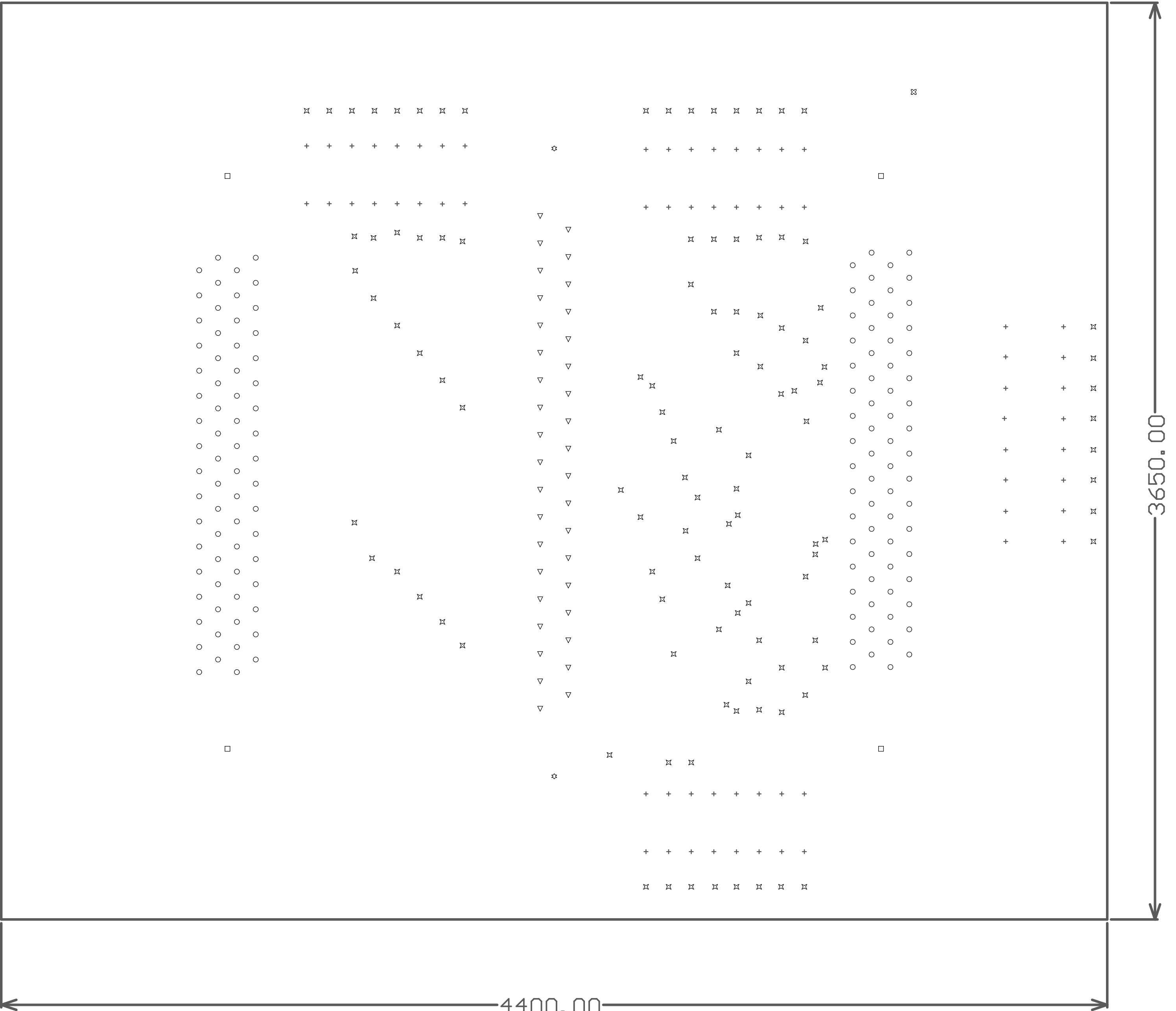


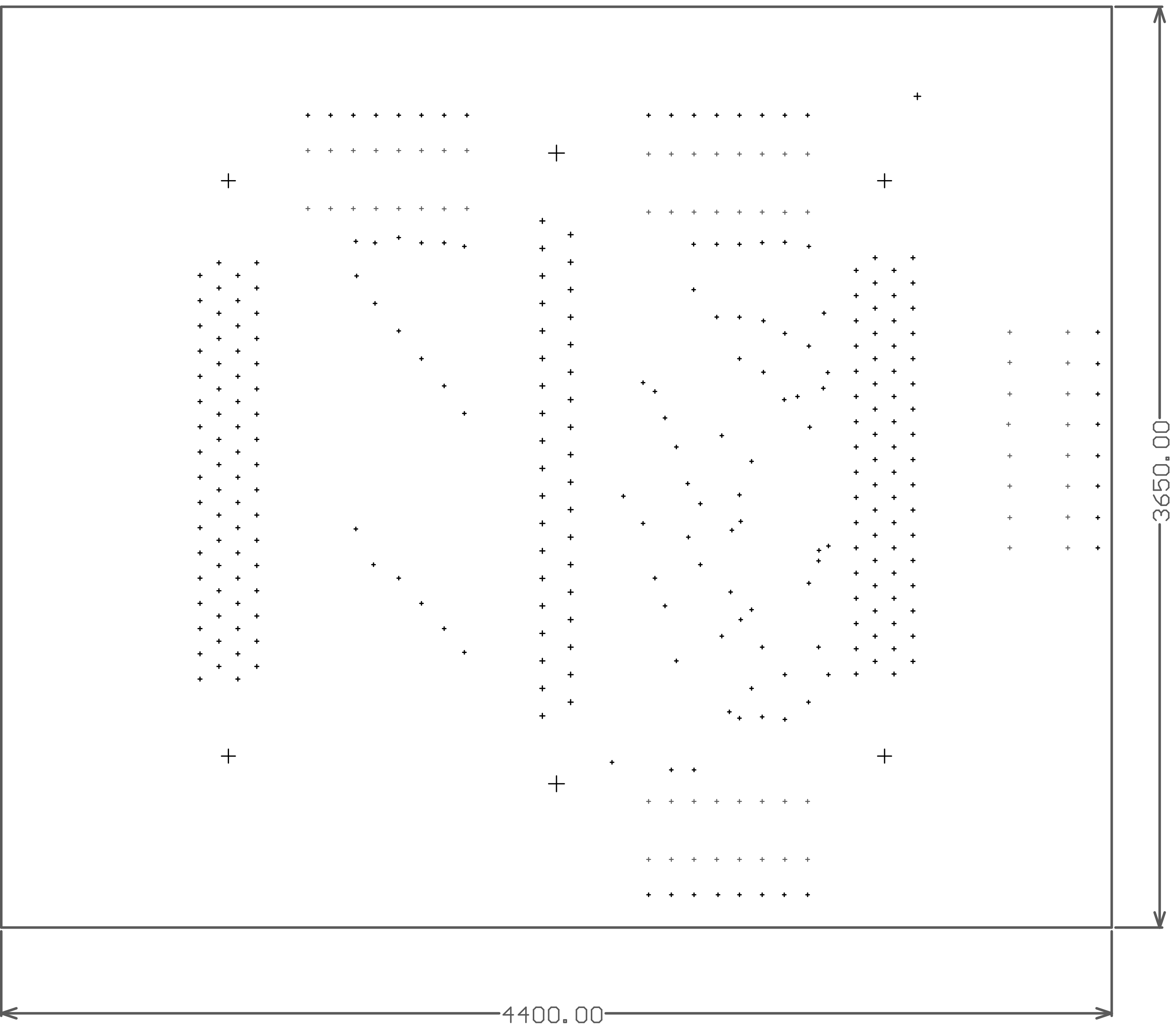






Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Via/Pad	Pad Shape	Template	Description	Hole Tolerance (+)	Hole Tolerance (-)
⌘	1	52.00mil (1.321mm)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c475h132			
☆	2	125.00mil (3.175mm)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c470h318			
□	4	110.24mil (2.800mm)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c550h280			
▽	37	40.00mil (1.016mm)	PTH	Round	Top Layer - Bottom Layer	Pad	(Mixed)	(Mixed)			
✕	109	14.00mil (0.356mm)	PTH	Round	Top Layer - Bottom Layer	Via	Rounded	v71h36			
○	136	31.50mil (0.800mm)	PTH	Round	Top Layer - Bottom Layer	Pad	(Mixed)	(Mixed)			
	289 Total										





Design Rules Verification Report

Filename : C:\Users\Public\Documents\Altium\Projects\NS18235 PXI 6723\NS18235 PXI 6723

Warnings 0
Rule Violations 0

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=10mil) (All),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ((All))	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=13mil) (Max=60mil) (Preferred=15mil) (All)	0
Power Plane Connect Rule(Relief Connect)(Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4)	0
Hole Size Constraint (Min=1mil) (Max=200mil) (All)	0
Pads and Vias to follow the Drill pairs settings	0
Hole To Hole Clearance (Gap=10mil) (All),(All)	0
Net Antennae (Tolerance=0mil) (All)	0
Height Constraint (Min=0mil) (Max=1000mil) (Preferred=500mil) (All)	0
Total	0

Electrical Rules Check Report

Class	Document	Message
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 0 has multiple names (Net Label AO 0,Net Label AO 0,Sheet Entry I1-D1(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 0 has multiple names (Sheet Entry I1-D1(Passive),Net Label AO 0,Net Label AO 0)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 1 has multiple names (Net Label AO 1,Net Label AO 1,Sheet Entry I1-D2(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 1 has multiple names (Sheet Entry I1-D2(Passive),Net Label AO 1,Net Label AO 1)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 2 has multiple names (Net Label AO 2,Net Label AO 2,Sheet Entry I1-D3(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 2 has multiple names (Sheet Entry I1-D3(Passive),Net Label AO 2,Net Label AO 2)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 3 has multiple names (Net Label AO 3,Net Label AO 3,Sheet Entry I1-D4(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 3 has multiple names (Sheet Entry I1-D4(Passive),Net Label AO 3,Net Label AO 3)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 4 has multiple names (Net Label AO 4,Net Label AO 4,Sheet Entry I1-D5(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 4 has multiple names (Sheet Entry I1-D5(Passive),Net Label AO 4,Net Label AO 4)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 5 has multiple names (Net Label AO 5,Net Label AO 5,Sheet Entry I1-D6(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 5 has multiple names (Sheet Entry I1-D6(Passive),Net Label AO 5,Net Label AO 5)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 6 has multiple names (Net Label AO 6,Net Label AO 6,Sheet Entry I1-D7(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 6 has multiple names (Sheet Entry I1-D7(Passive),Net Label AO 6,Net Label AO 6)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 7 has multiple names (Net Label AO 7,Net Label AO 7,Sheet Entry I1-D8(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 7 has multiple names (Sheet Entry I1-D8(Passive),Net Label AO 7,Net Label AO 7)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 8 has multiple names (Net Label AO 8,Net Label AO 8,Sheet Entry I3-D1(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 8 has multiple names (Sheet Entry I3-D1(Passive),Net Label AO 8,Net Label AO 8)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 9 has multiple names (Net Label AO 9,Net Label AO 9,Sheet Entry I3-D2(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 9 has multiple names (Sheet Entry I3-D2(Passive),Net Label AO 9,Net Label AO 9)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 10 has multiple names (Net Label AO 10,Net Label AO 10,Sheet Entry I3-D3(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 10 has multiple names (Sheet Entry I3-D3(Passive),Net Label AO 10,Net Label AO 10)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 11 has multiple names (Net Label AO 11,Net Label AO 11,Sheet Entry I3-D4(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 11 has multiple names (Sheet Entry I3-D4(Passive),Net Label AO 11,Net Label AO 11)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 12 has multiple names (Net Label AO 12,Net Label AO 12,Sheet Entry I3-D5(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 12 has multiple names (Sheet Entry I3-D5(Passive),Net Label AO 12,Net Label AO 12)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 13 has multiple names (Net Label AO 13,Net Label AO 13,Sheet Entry I3-D6(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 13 has multiple names (Sheet Entry I3-D6(Passive),Net Label AO 13,Net Label AO 13)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 14 has multiple names (Net Label AO 14,Net Label AO 14,Sheet Entry I3-D7(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 14 has multiple names (Sheet Entry I3-D7(Passive),Net Label AO 14,Net Label AO 14)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 15 has multiple names (Net Label AO 15,Net Label AO 15,Sheet Entry I3-D8(Passive))

Class	Document	Message
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 15 has multiple names (Sheet Entry I3-D8(Passive),Net Label AO 15,Net Label AO 15)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 16 has multiple names (Net Label AO 16,Net Label AO 16,Sheet Entry I2-D1(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 16 has multiple names (Sheet Entry I2-D1(Passive),Net Label AO 16,Net Label AO 16)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 17 has multiple names (Net Label AO 17,Net Label AO 17,Sheet Entry I2-D2(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 17 has multiple names (Sheet Entry I2-D2(Passive),Net Label AO 17,Net Label AO 17)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 18 has multiple names (Net Label AO 18,Net Label AO 18,Sheet Entry I2-D3(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 18 has multiple names (Sheet Entry I2-D3(Passive),Net Label AO 18,Net Label AO 18)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 19 has multiple names (Net Label AO 19,Net Label AO 19,Sheet Entry I2-D4(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 19 has multiple names (Sheet Entry I2-D4(Passive),Net Label AO 19,Net Label AO 19)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 20 has multiple names (Net Label AO 20,Net Label AO 20,Sheet Entry I2-D5(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 20 has multiple names (Sheet Entry I2-D5(Passive),Net Label AO 20,Net Label AO 20)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 21 has multiple names (Net Label AO 21,Net Label AO 21,Sheet Entry I2-D6(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 21 has multiple names (Sheet Entry I2-D6(Passive),Net Label AO 21,Net Label AO 21)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 22 has multiple names (Net Label AO 22,Net Label AO 22,Sheet Entry I2-D7(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 22 has multiple names (Sheet Entry I2-D7(Passive),Net Label AO 22,Net Label AO 22)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 23 has multiple names (Net Label AO 23,Net Label AO 23,Sheet Entry I2-D8(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 23 has multiple names (Sheet Entry I2-D8(Passive),Net Label AO 23,Net Label AO 23)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 24 has multiple names (Net Label AO 24,Net Label AO 24,Sheet Entry I4-D1(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 24 has multiple names (Sheet Entry I4-D1(Passive),Net Label AO 24,Net Label AO 24)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 25 has multiple names (Net Label AO 25,Net Label AO 25,Sheet Entry I4-D2(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 25 has multiple names (Sheet Entry I4-D2(Passive),Net Label AO 25,Net Label AO 25)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 26 has multiple names (Net Label AO 26,Net Label AO 26,Sheet Entry I4-D3(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 26 has multiple names (Sheet Entry I4-D3(Passive),Net Label AO 26,Net Label AO 26)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 27 has multiple names (Net Label AO 27,Net Label AO 27,Sheet Entry I4-D4(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 27 has multiple names (Sheet Entry I4-D4(Passive),Net Label AO 27,Net Label AO 27)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 28 has multiple names (Net Label AO 28,Net Label AO 28,Sheet Entry I4-D5(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 28 has multiple names (Sheet Entry I4-D5(Passive),Net Label AO 28,Net Label AO 28)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 29 has multiple names (Net Label AO 29,Net Label AO 29,Sheet Entry I4-D6(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 29 has multiple names (Sheet Entry I4-D6(Passive),Net Label AO 29,Net Label AO 29)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 30 has multiple names (Net Label AO 30,Net Label AO 30,Sheet Entry I4-D7(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 30 has multiple names (Sheet Entry I4-D7(Passive),Net Label AO 30,Net Label AO 30)
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 31 has multiple names (Net Label AO 31,Net Label AO 31,Sheet Entry I4-D8(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO 31 has multiple names (Sheet Entry I4-D8(Passive),Net Label AO 31,Net Label AO 31)

Class	Document	Message
Warning	NS18235 PXI 6723.SchDoc	AO 31) Nets Wire AO GND has multiple names (Net Label AO GND,Net Label AO GND,Power Object AO GND,Sheet Entry I1-COMM(Passive),Sheet Entry I2-COMM(Passive),Sheet Entry I3-COMM(Passive),Sheet Entry I4-COMM(Passive))
Warning	NS18235 PXI 6723.SchDoc	Nets Wire AO GND has multiple names (Sheet Entry I1-COMM(Passive),Sheet Entry I2-COMM(Passive),Sheet Entry I3-COMM(Passive),Sheet Entry I4-COMM(Passive),Net Label AO GND,Net Label AO GND,Power Object AO GND)

Quantity	Value	Description	Designator	Footprint	LibRef
32	LED GRN-YEL 1206	Back to back LEDs	D00, D01, D02, D03, D04, D05, D06, D07, D08, D09, D10, D11, D12, D13, D14, D15, D16, D17, D18, D19, D20, D21, D22, D23, D24, D25, D26, D27, D28, D29, D30, D31	1206 LED	LED GRN_YEL
2	DSUB 68 PIN 050 RECEPT VERT	Receptacle Header Assembly	J1, J2	CHAMP1.27-2V68	DSUB 68 PIN 050 RECEPT VERT
1	DSUB 37P VRT F PCB		J3	DSUB1.385-2H37A FEMALE VRT	dsub 37 pin VRT pcb Female
32	1.6K	Resistor	R1, R2, R3, R4, R5, R6, R7, R8, R9, R10, R11, R12, R13, R14, R15, R16, R17, R18, R19, R20, R21, R22, R23, R24, R25, R26, R27, R28, R29, R30, R31, R32	1206 Passive	Resistor
1	Test Point Jack	Through Hole Test Point, Snap-In 052 Hole	TP GND1	Ground Test Point	Test Point Jack TH